

FIG. 7

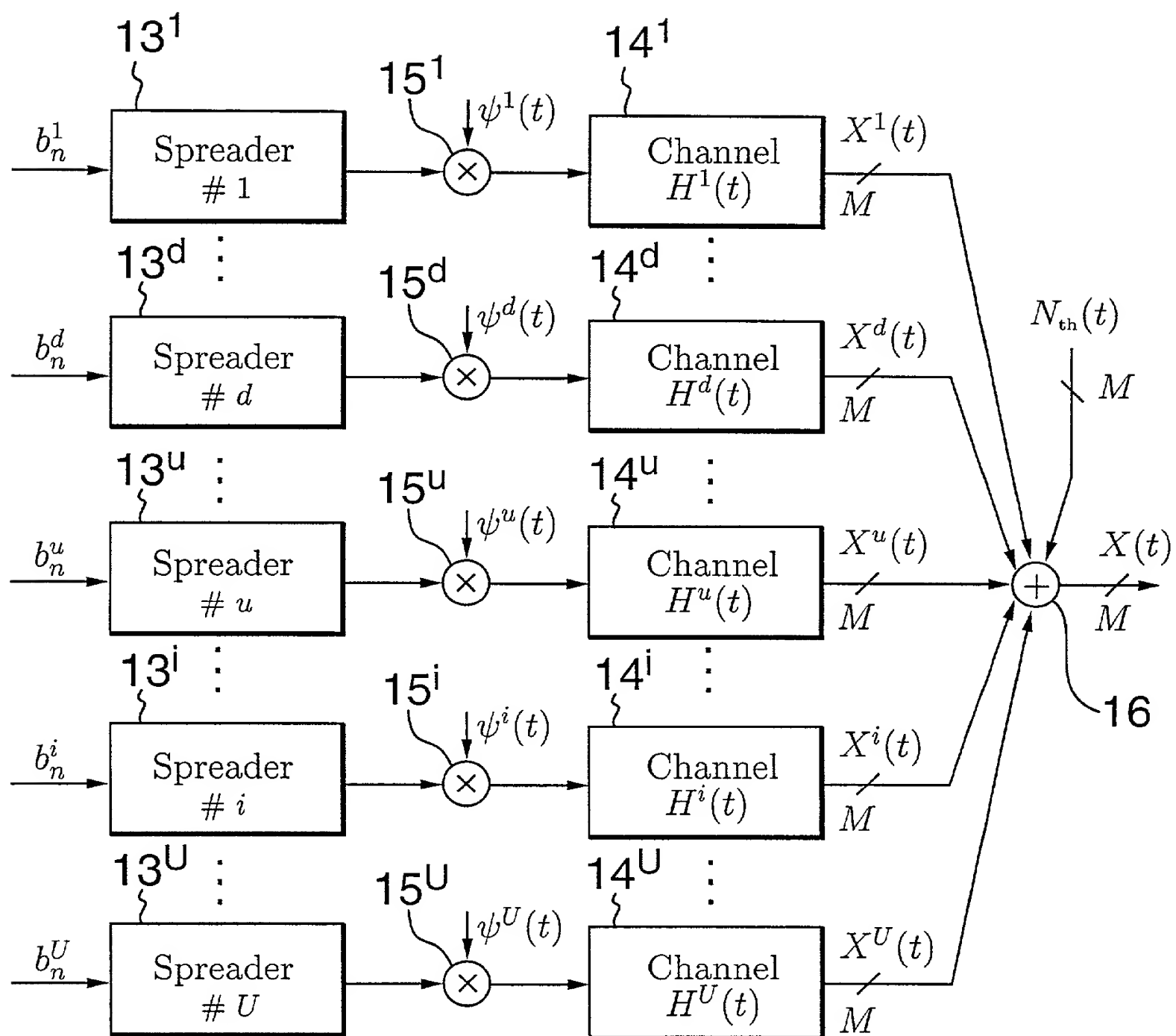


FIG. 2

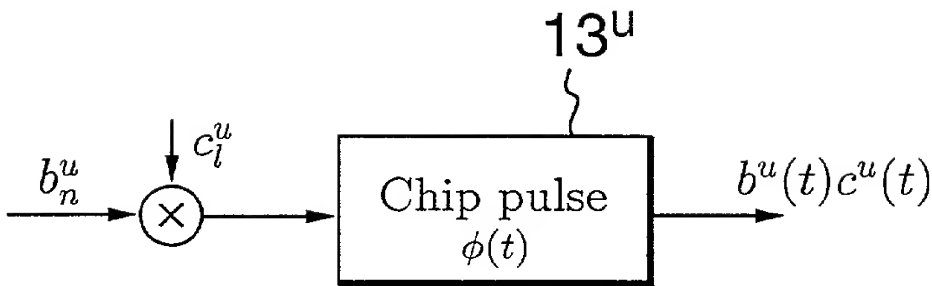


FIG. 3

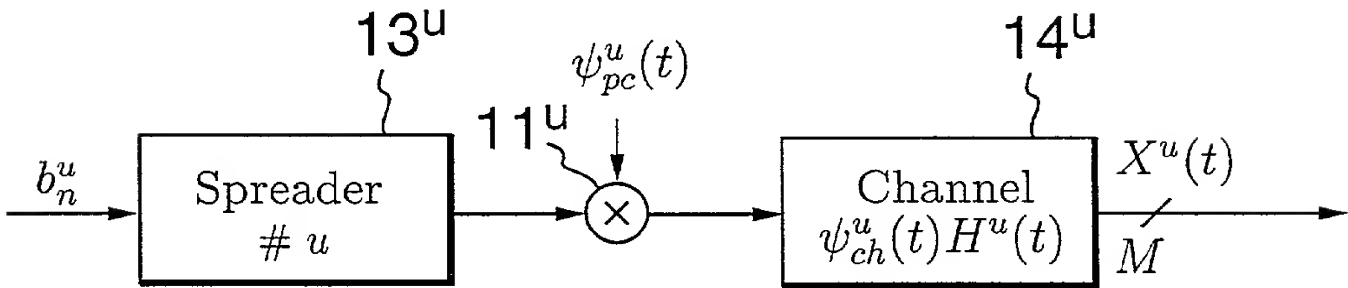


FIG. 4(a)  $\equiv$

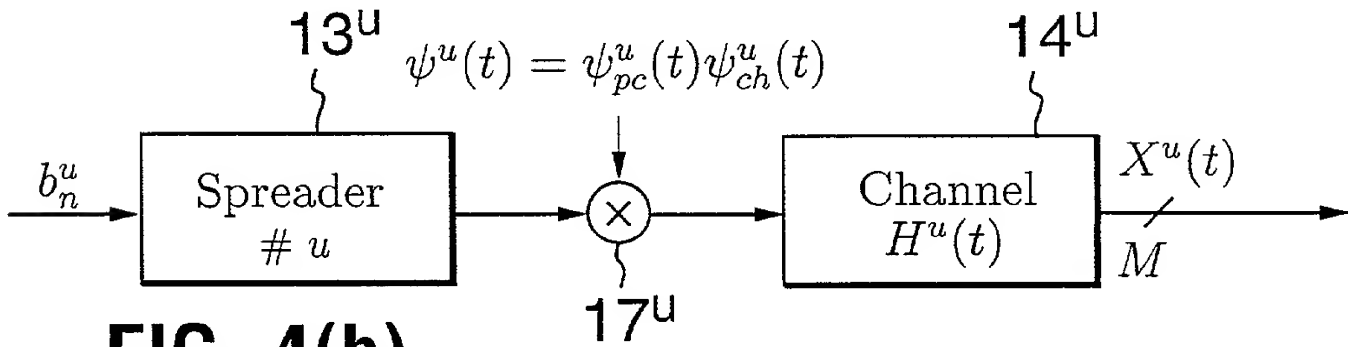
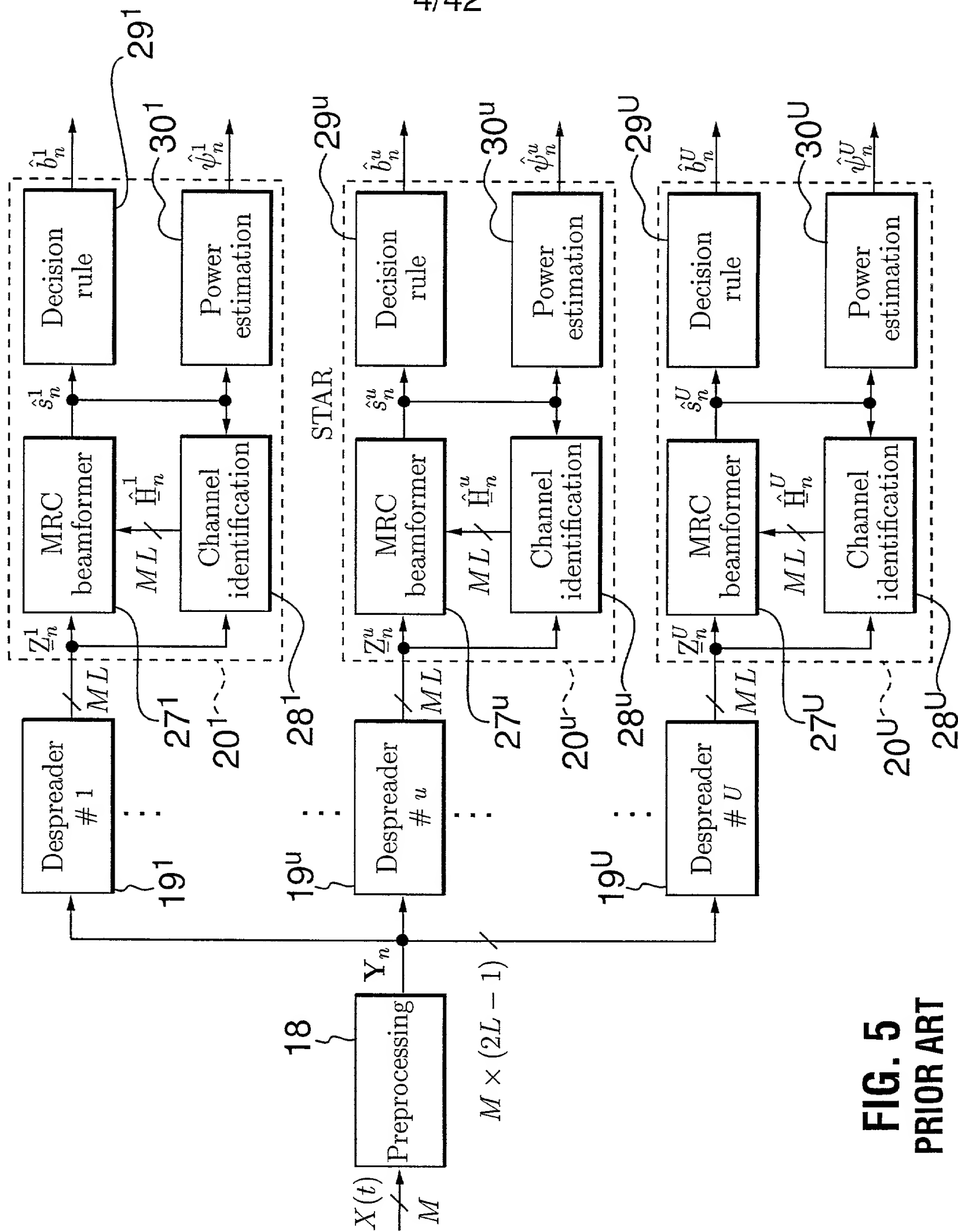
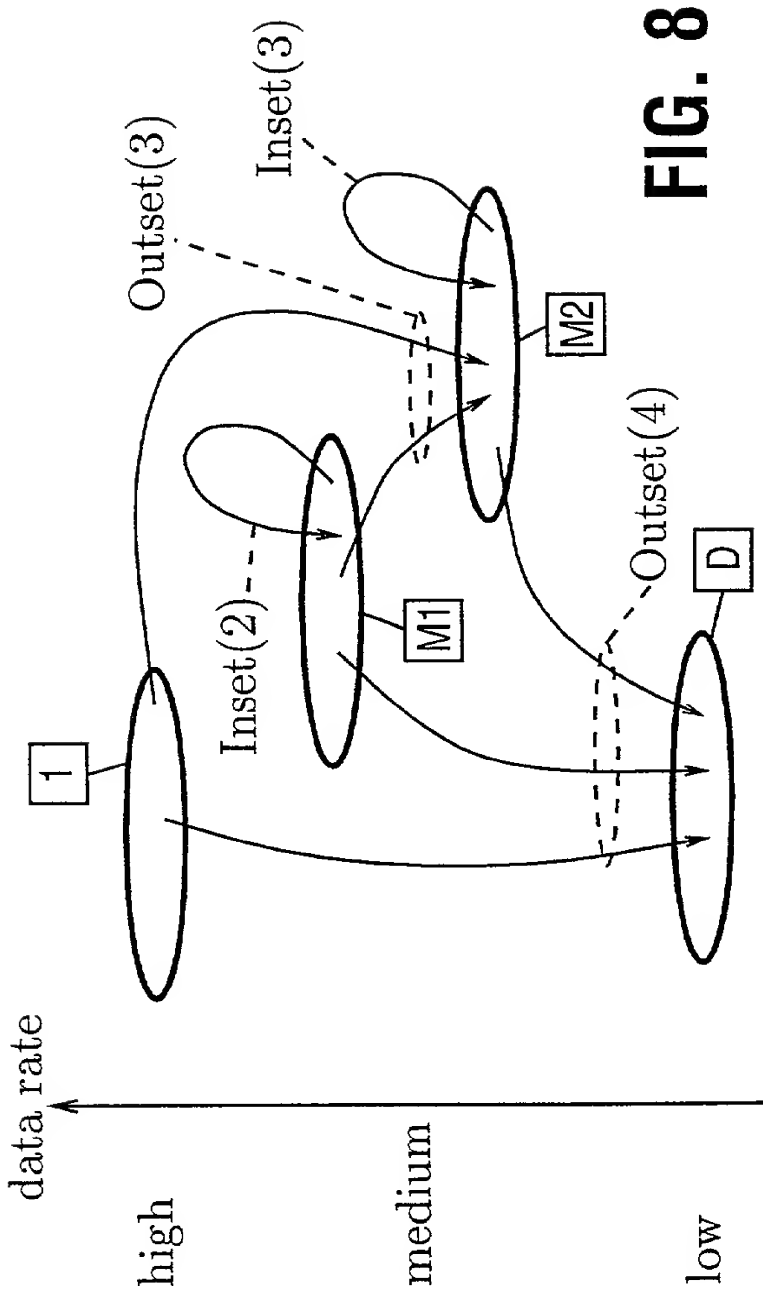
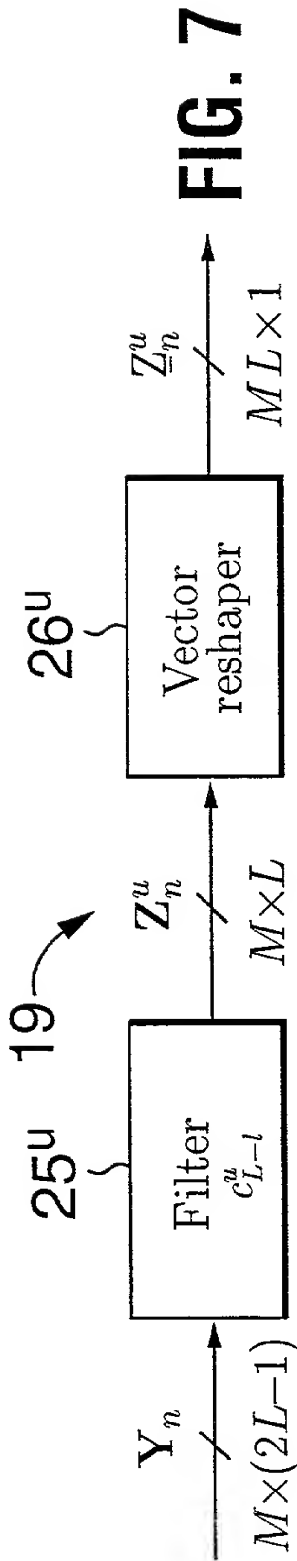
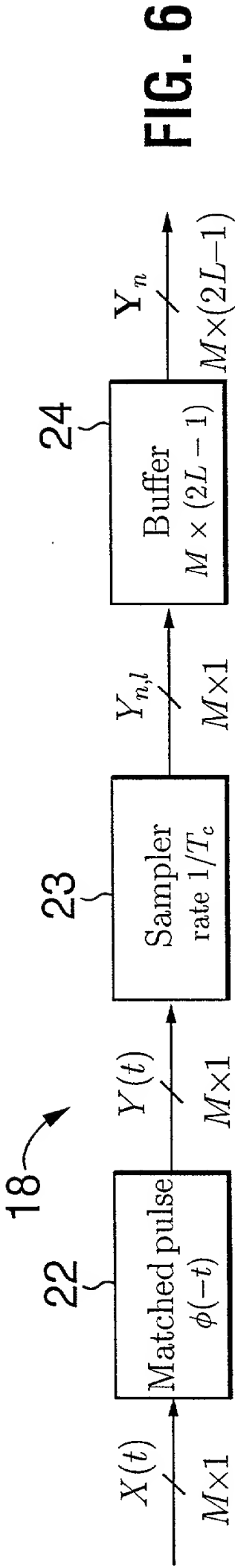


FIG. 4(b)



**FIG. 5**  
PRIOR ART





to receivers for other desired users

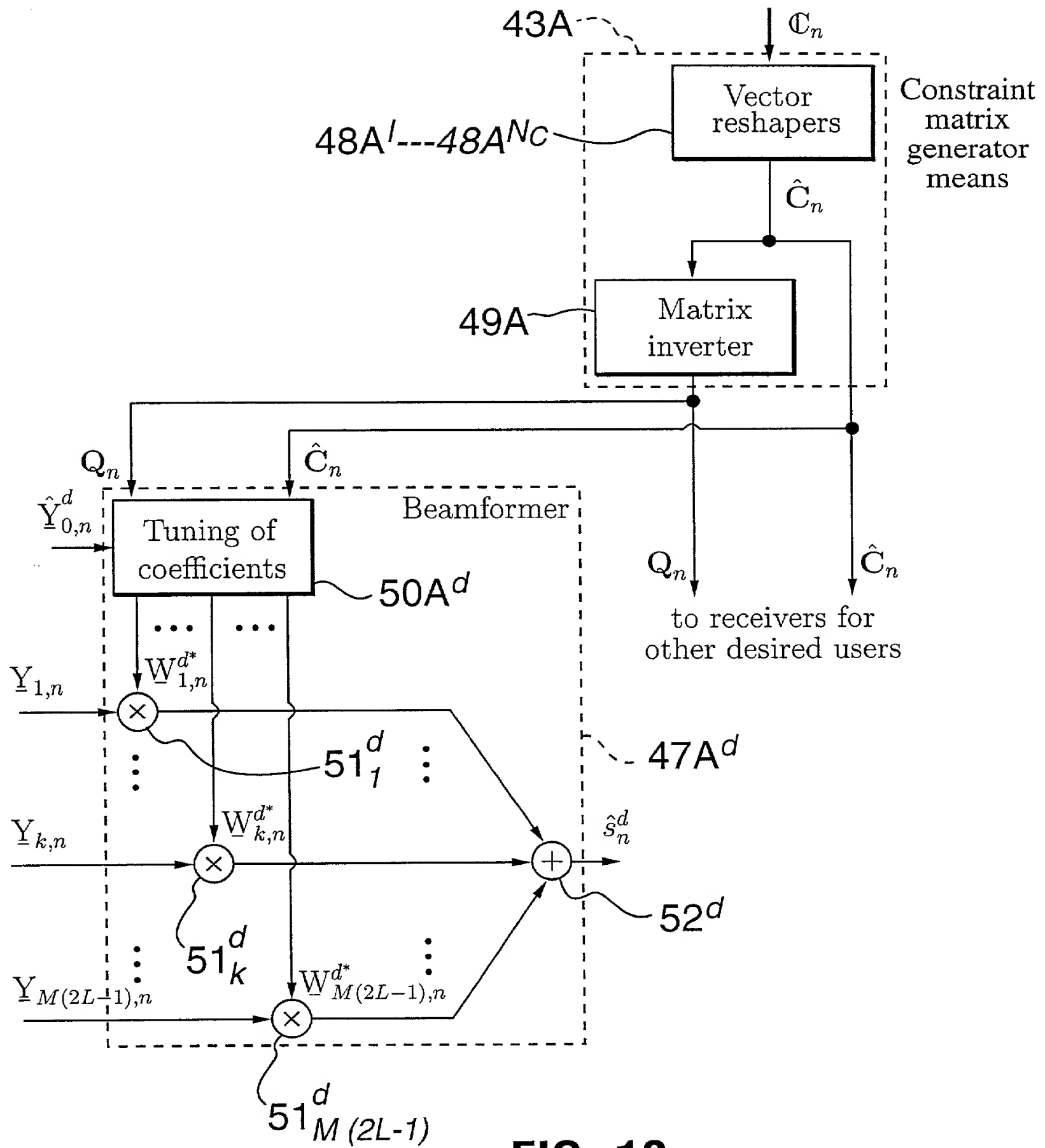
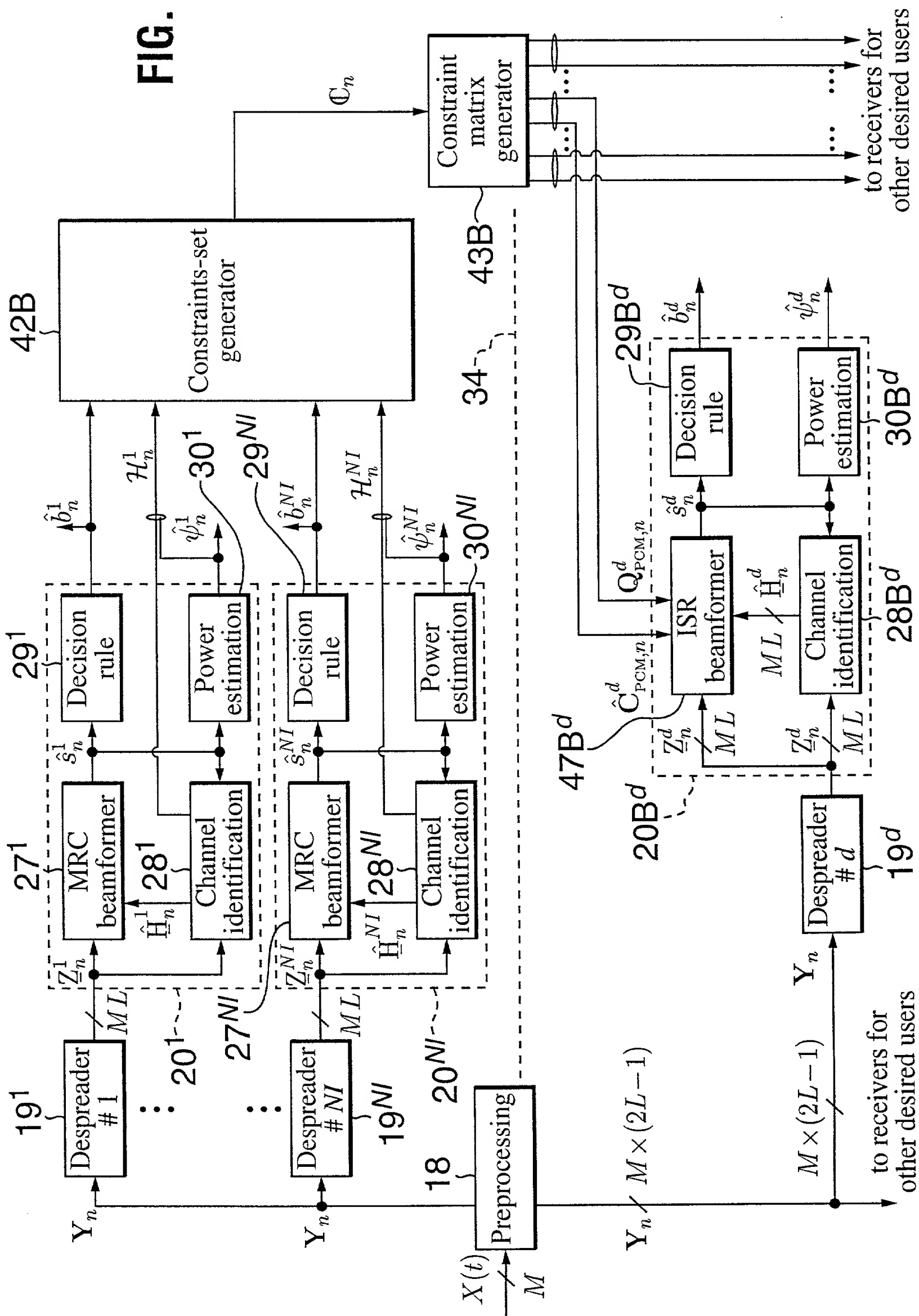


FIG. 10

FIG. 11





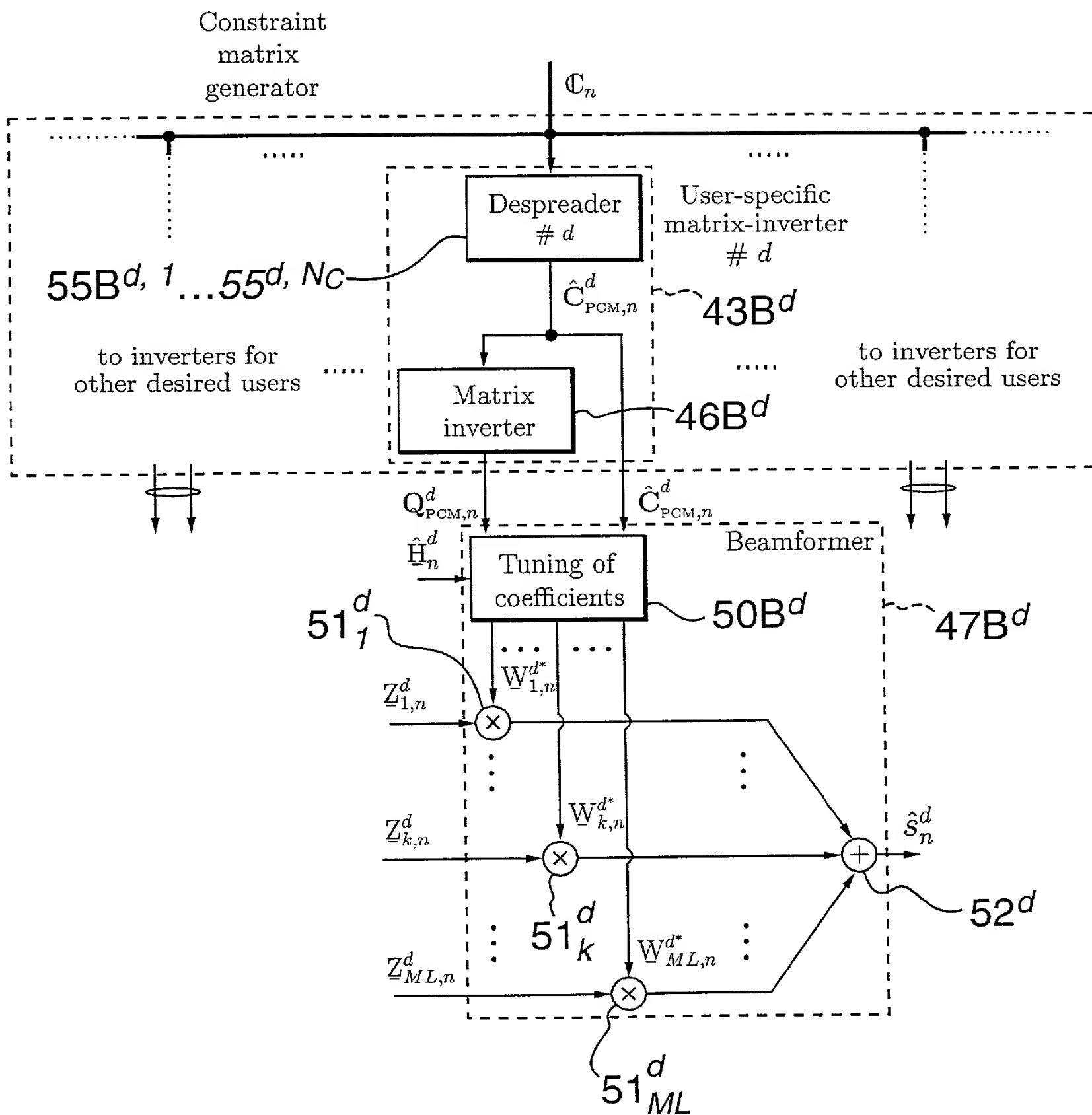


FIG. 12

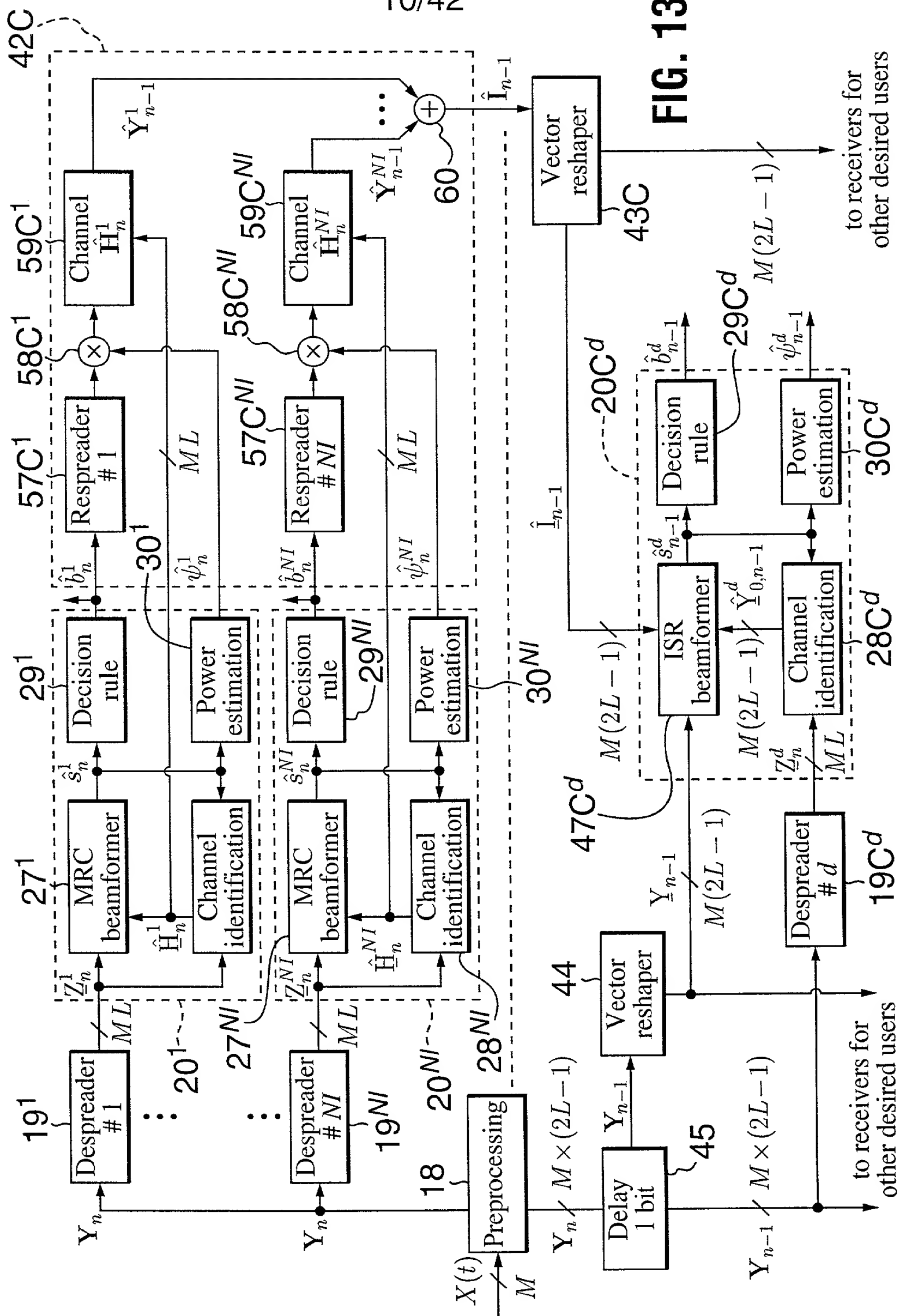


FIG. 13

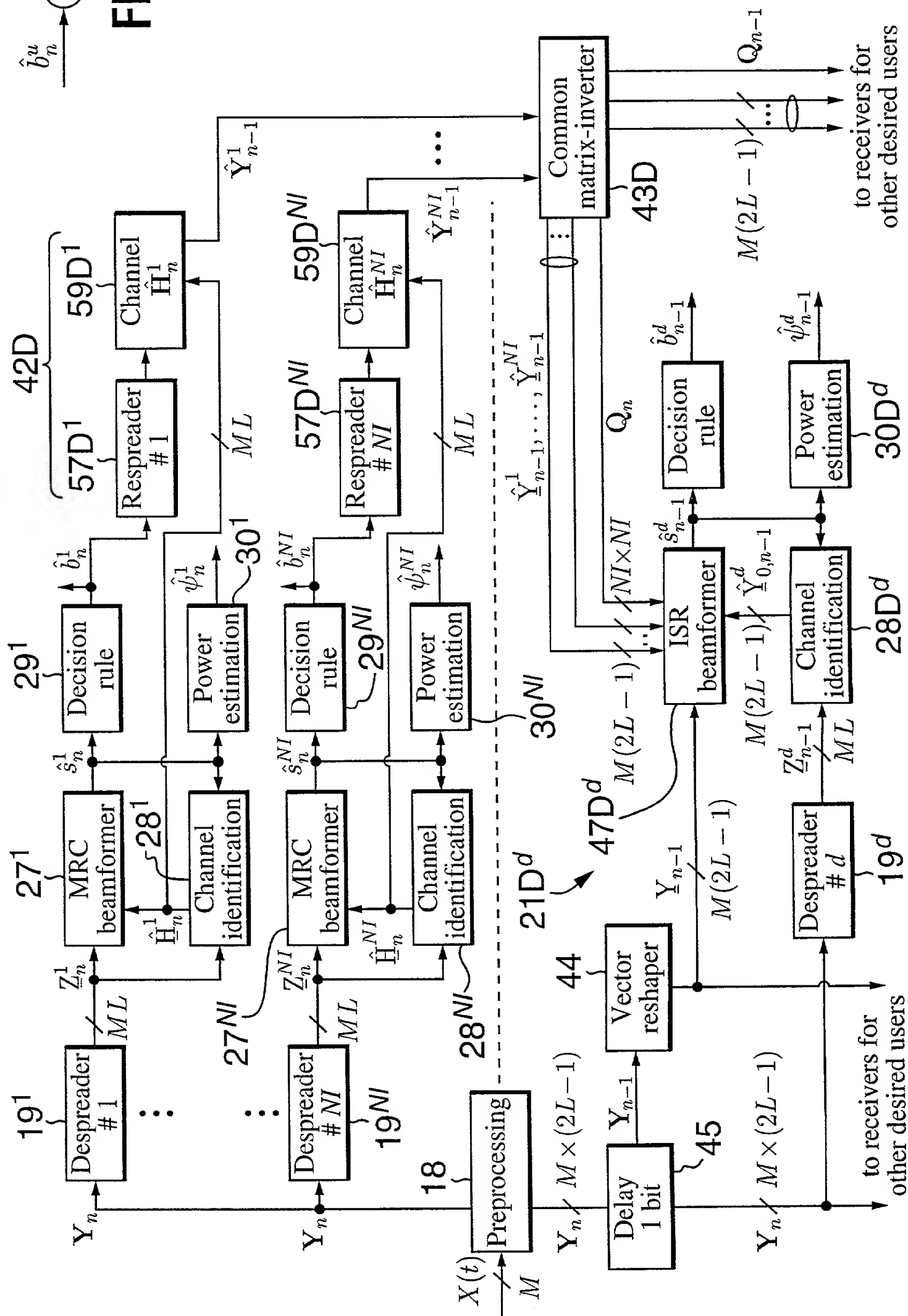
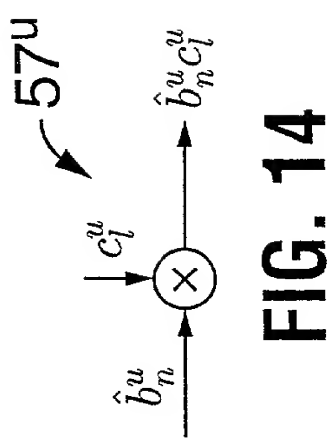
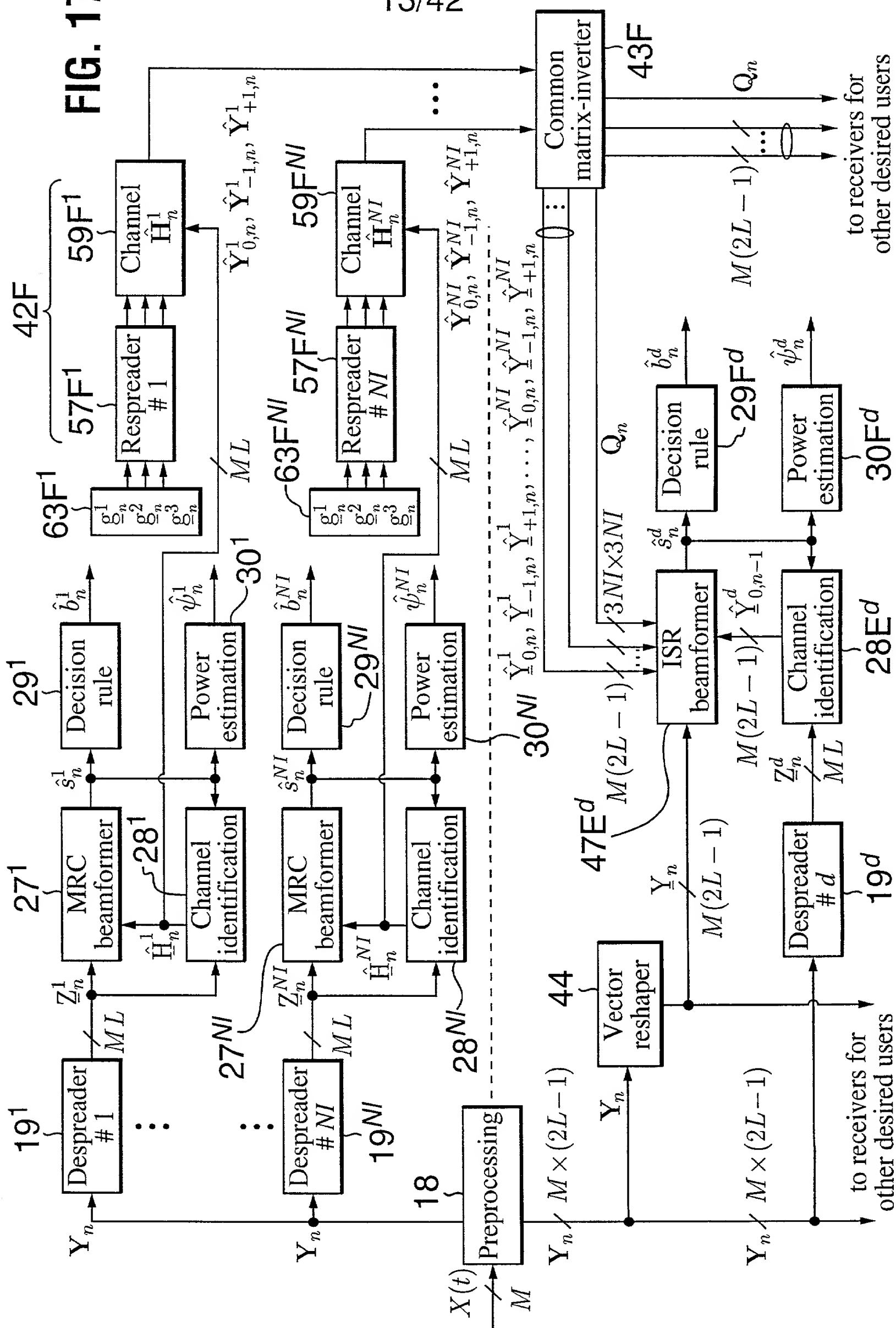


FIG. 15

[illegible]

FIG. 17



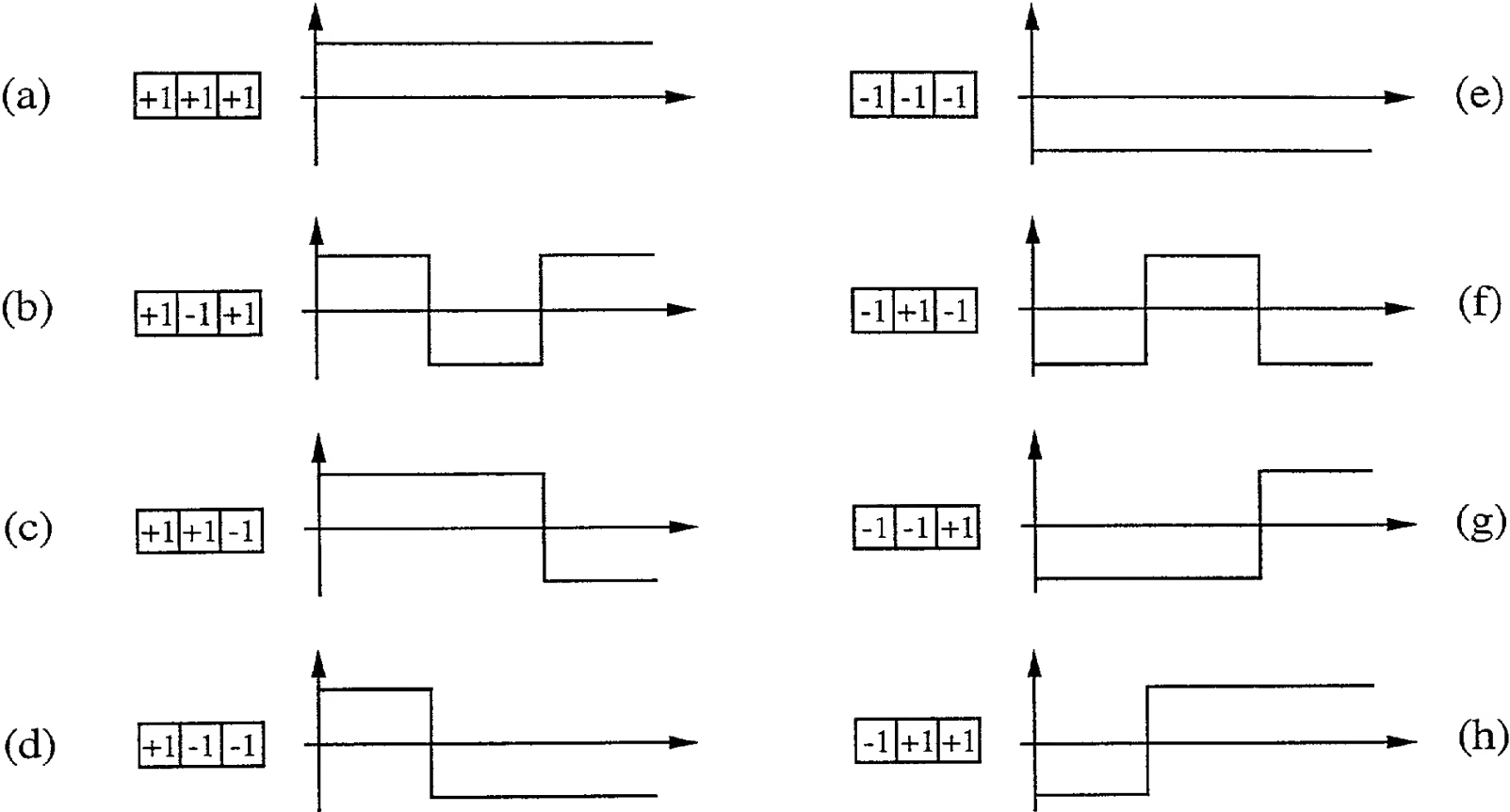


FIG. 18

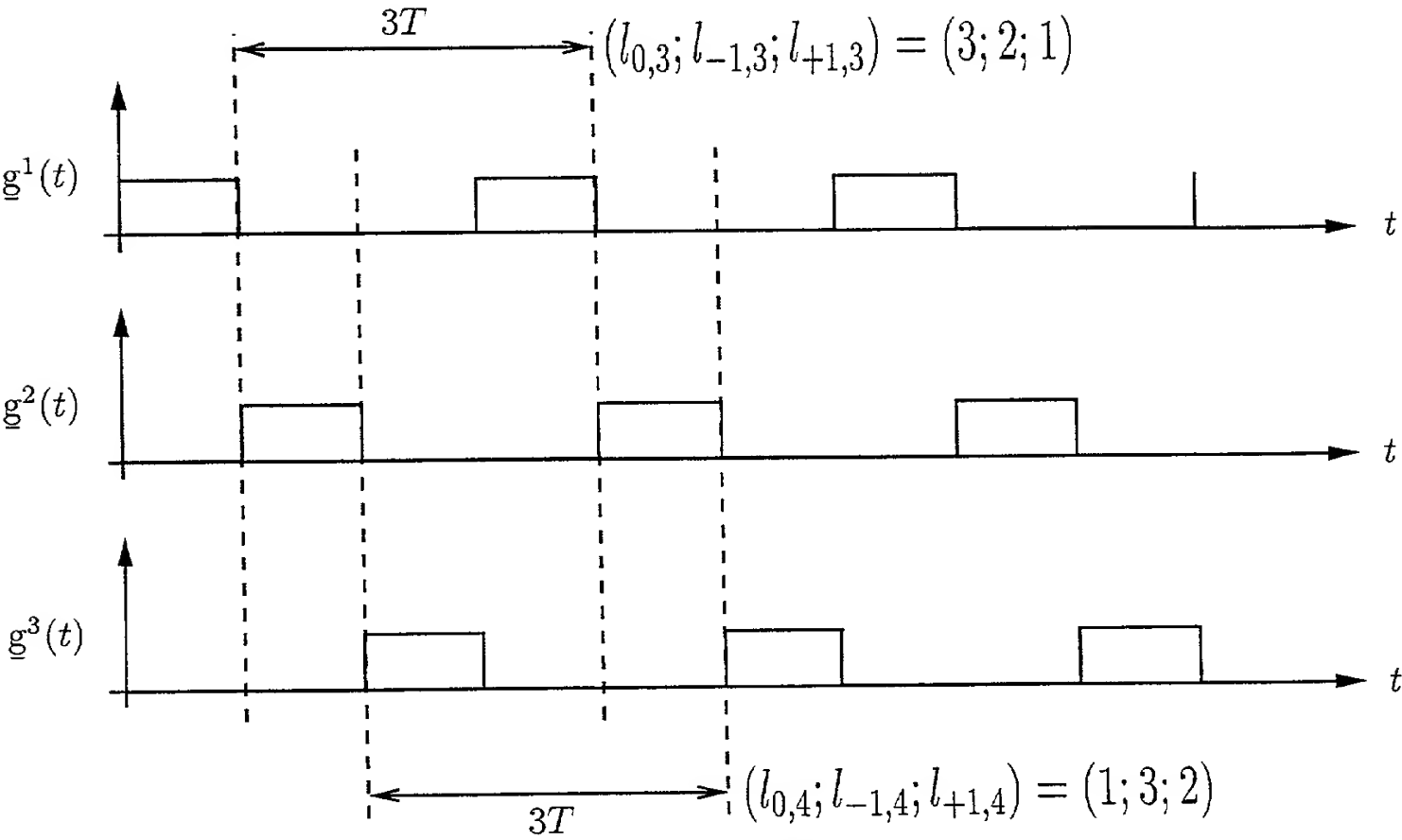
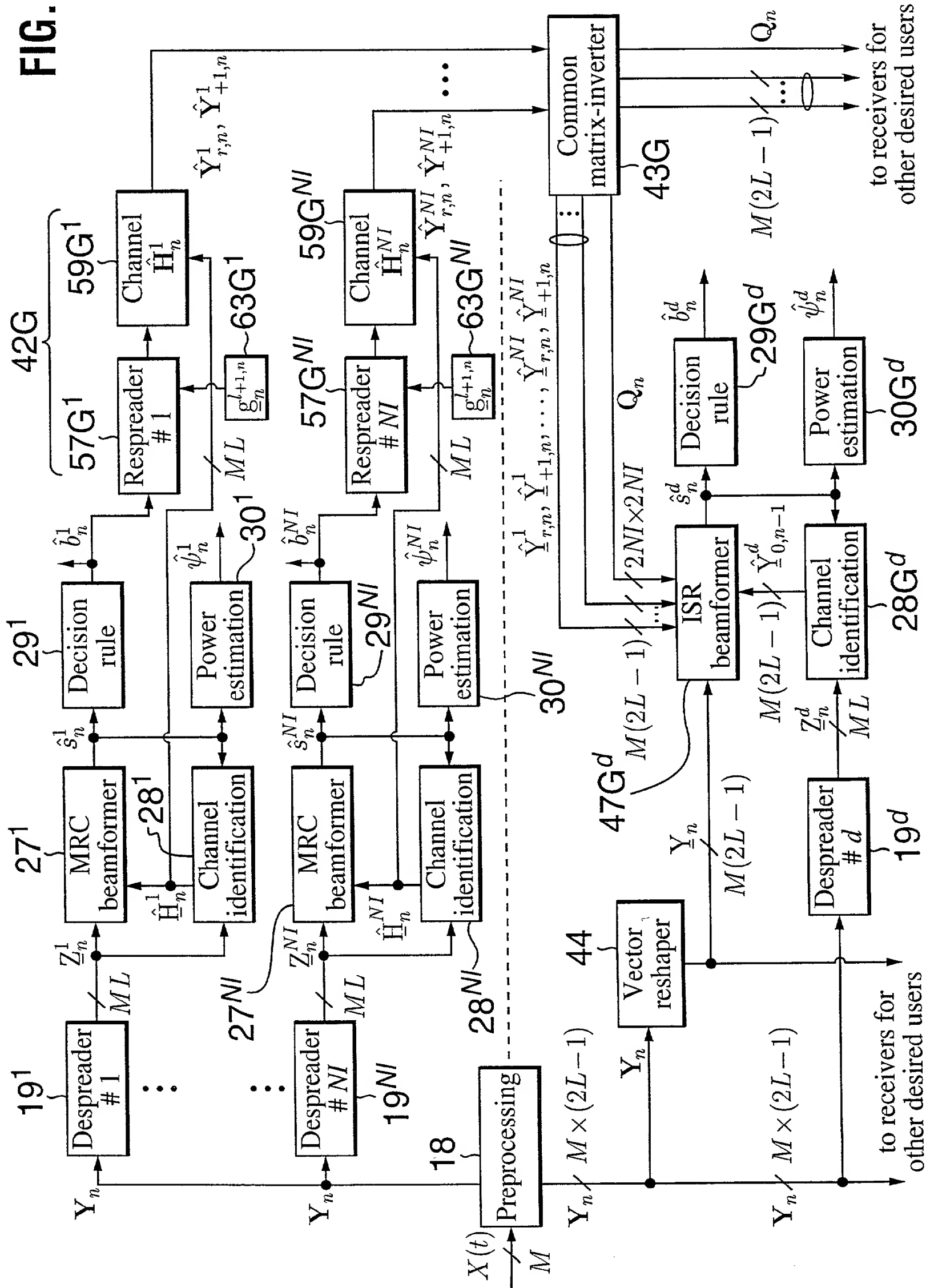


FIG. 19

FIG. 20



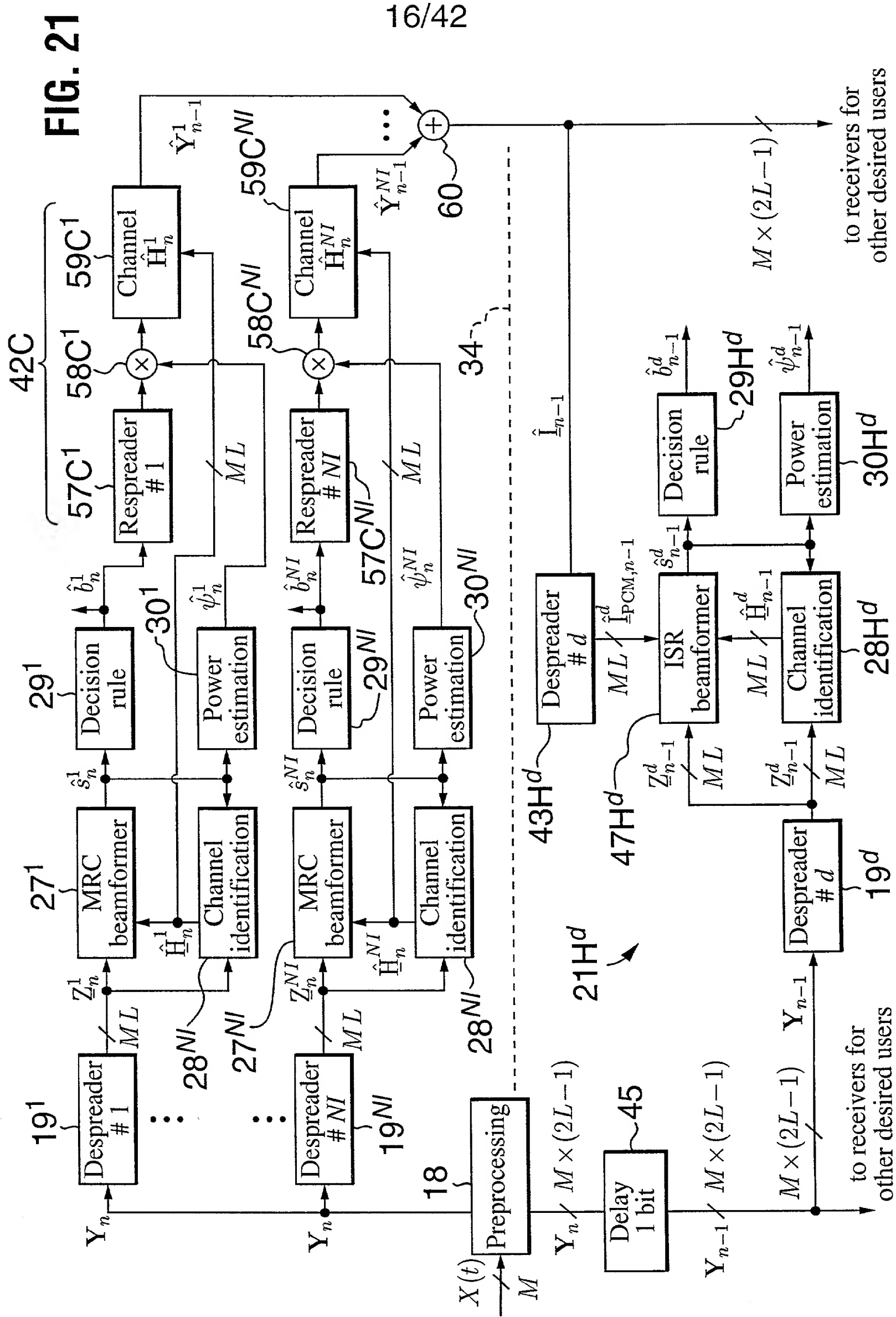
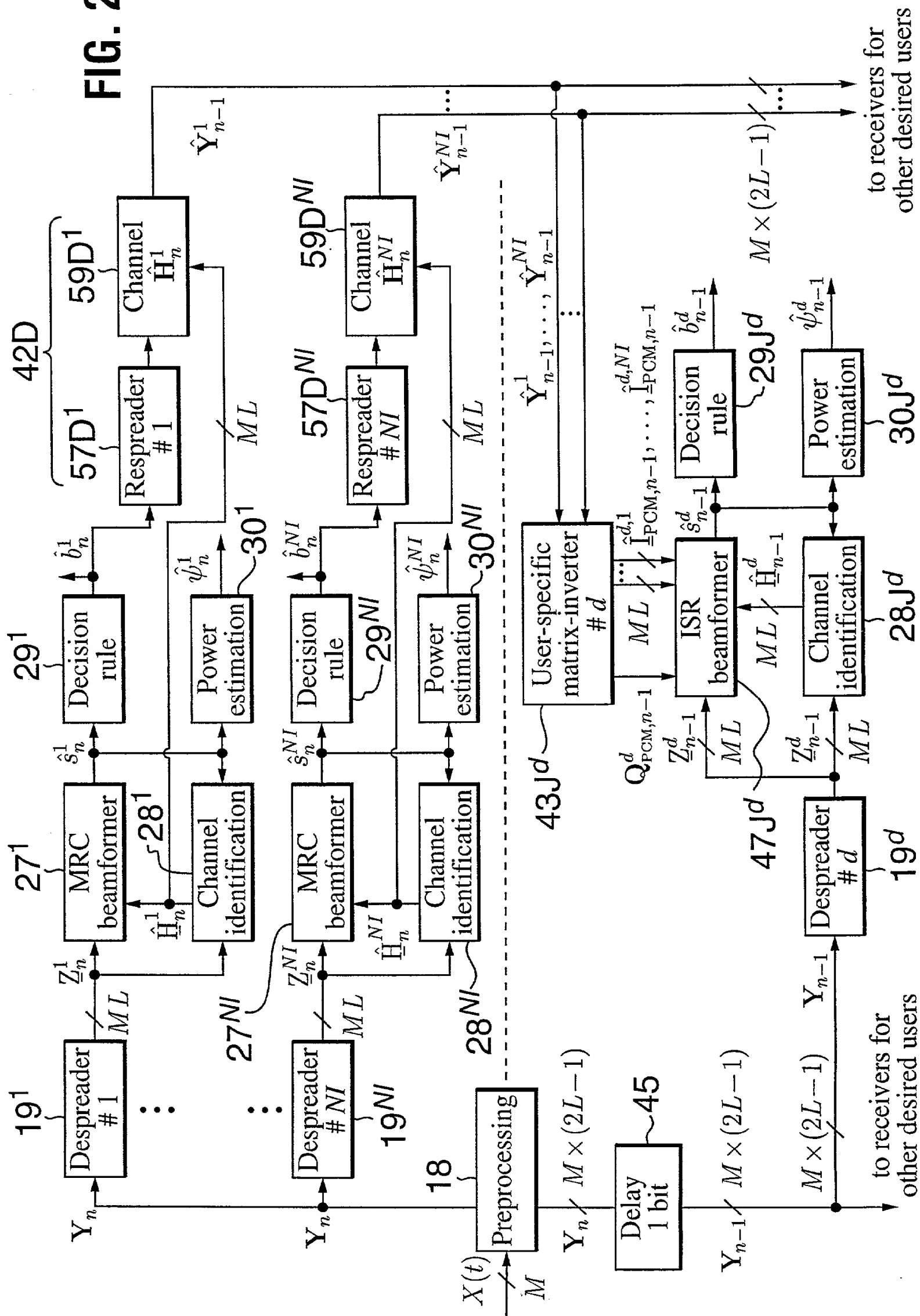




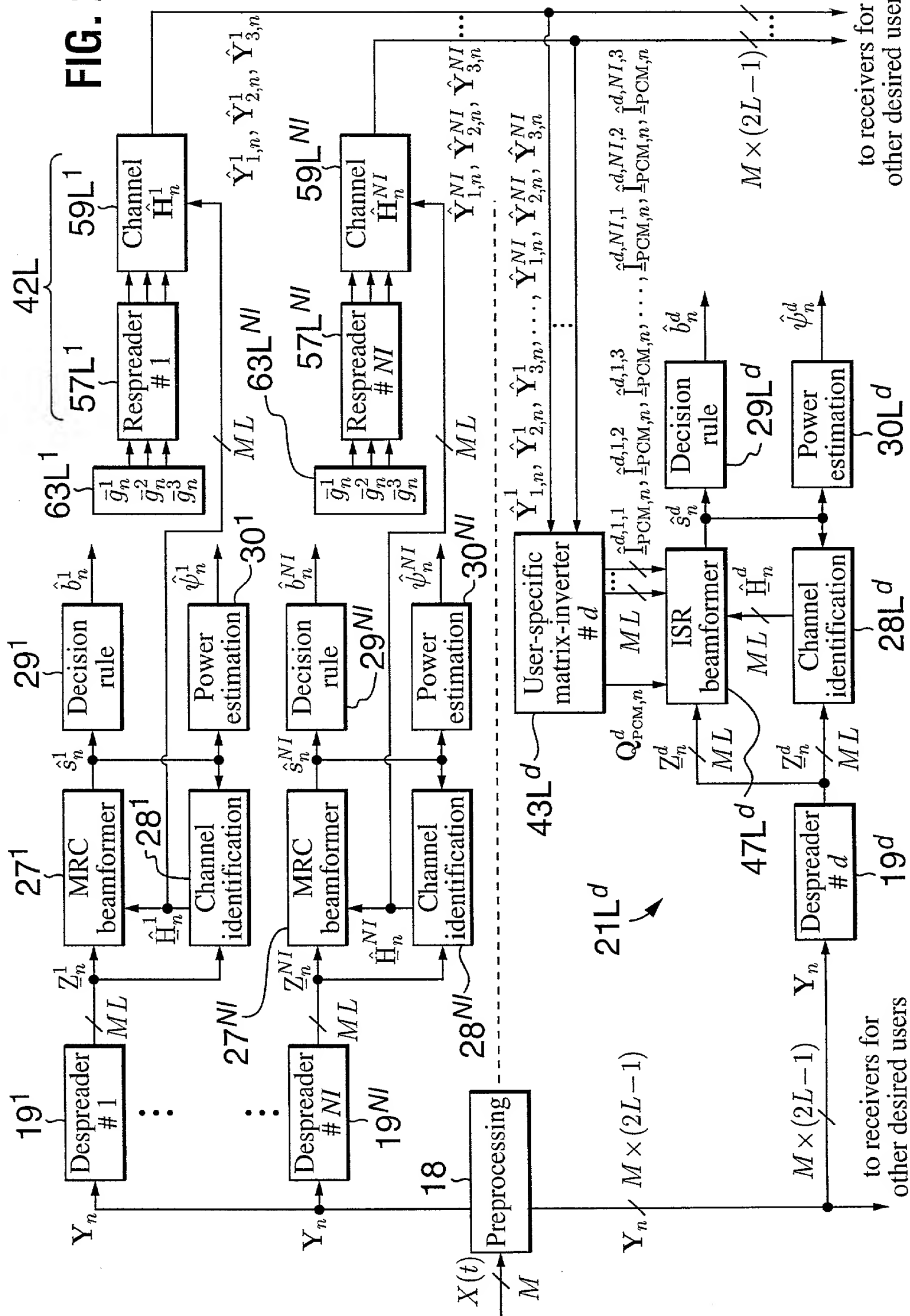
FIG. 22



**FIG. 2**

The diagram illustrates a multi-user MIMO system architecture. The input signal  $X(t)$  is processed by a Preprocessing block (18) to produce  $Y_n$  with dimensions  $M \times (2L-1)$ . This signal is then distributed to  $NI$  parallel processing paths, labeled  $19^1$  through  $19^{NI}$ . Each path  $19^i$  consists of a Despreader #  $i$  (19<sup>i</sup>) receiving  $Y_n$  and outputting  $Z_n^i$  (dimension  $ML$ ). The  $Z_n^i$  signal is fed into an MRC beamformer (27<sup>i</sup>) and a Channel identification block (28<sup>i</sup>). The MRC beamformer (27<sup>i</sup>) also receives  $\hat{H}_n^i$  and outputs  $\hat{s}_n^i$ . The Channel identification block (28<sup>i</sup>) receives  $\hat{H}_n^i$  and outputs  $\hat{\psi}_n^i$ . The  $\hat{s}_n^i$  signal is then processed by a Decision rule (29<sup>i</sup>) and a Power estimation block (30<sup>i</sup>). The Power estimation block (30<sup>i</sup>) outputs  $\hat{\psi}_n^i$  to the Channel identification block (28<sup>i</sup>). The Decision rule (29<sup>i</sup>) outputs  $\hat{b}_n^i$  to the Respreader #  $i$  (57<sup>i</sup>). The Respreader #  $i$  (57<sup>i</sup>) receives  $\hat{b}_n^i$  and outputs  $\hat{Y}_{n-1,1}^i, \dots, \hat{Y}_{n-1,N_f}^i$  to the Sub-channels (59<sup>i</sup>). The Sub-channels (59<sup>i</sup>) receive  $\hat{Y}_{n-1,1}^i, \dots, \hat{Y}_{n-1,N_f}^i$  and output  $\hat{Y}_{n-1,1}^i, \dots, \hat{Y}_{n-1,N_f}^i$ . The  $\hat{Y}_{n-1,1}^i, \dots, \hat{Y}_{n-1,N_f}^i$  signals are then fed into a User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) and an ISR beamformer (47K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) receives  $\hat{Y}_{n-1,1}^i, \dots, \hat{Y}_{n-1,N_f}^i$  and outputs  $Q_{PCM,n-1}^d$  (dimension  $ML$ ). The ISR beamformer (47K<sup>d</sup>) receives  $Q_{PCM,n-1}^d$  and outputs  $\hat{s}_{n-1}^d$  (dimension  $ML$ ). The  $\hat{s}_{n-1}^d$  signal is then processed by a Decision rule (29K<sup>d</sup>) and a Power estimation block (30K<sup>d</sup>). The Decision rule (29K<sup>d</sup>) outputs  $\hat{b}_{n-1}^d$  to the Power estimation block (30K<sup>d</sup>). The Power estimation block (30K<sup>d</sup>) outputs  $\hat{\psi}_{n-1}^d$  to the Channel identification block (28K<sup>d</sup>). The Channel identification block (28K<sup>d</sup>) receives  $\hat{\psi}_{n-1}^d$  and outputs  $\hat{H}_{n-1}^d$  to the MRC beamformer (27K<sup>d</sup>). The MRC beamformer (27K<sup>d</sup>) receives  $\hat{H}_{n-1}^d$  and outputs  $\hat{s}_{n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) also receives  $\hat{H}_{n-1}^d$  and outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) also receives  $\hat{s}_{n-1}^d$  and outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K<sup>d</sup>) outputs  $Q_{PCM,n-1}^d$  to the ISR beamformer (47K<sup>d</sup>). The ISR beamformer (47K<sup>d</sup>) outputs  $\hat{s}_{n-1}^d$  to the User-specific matrix-inverter #  $d$  (43K<sup>d</sup>). The User-specific matrix-inverter #  $d$  (43K

**FIG. 24**



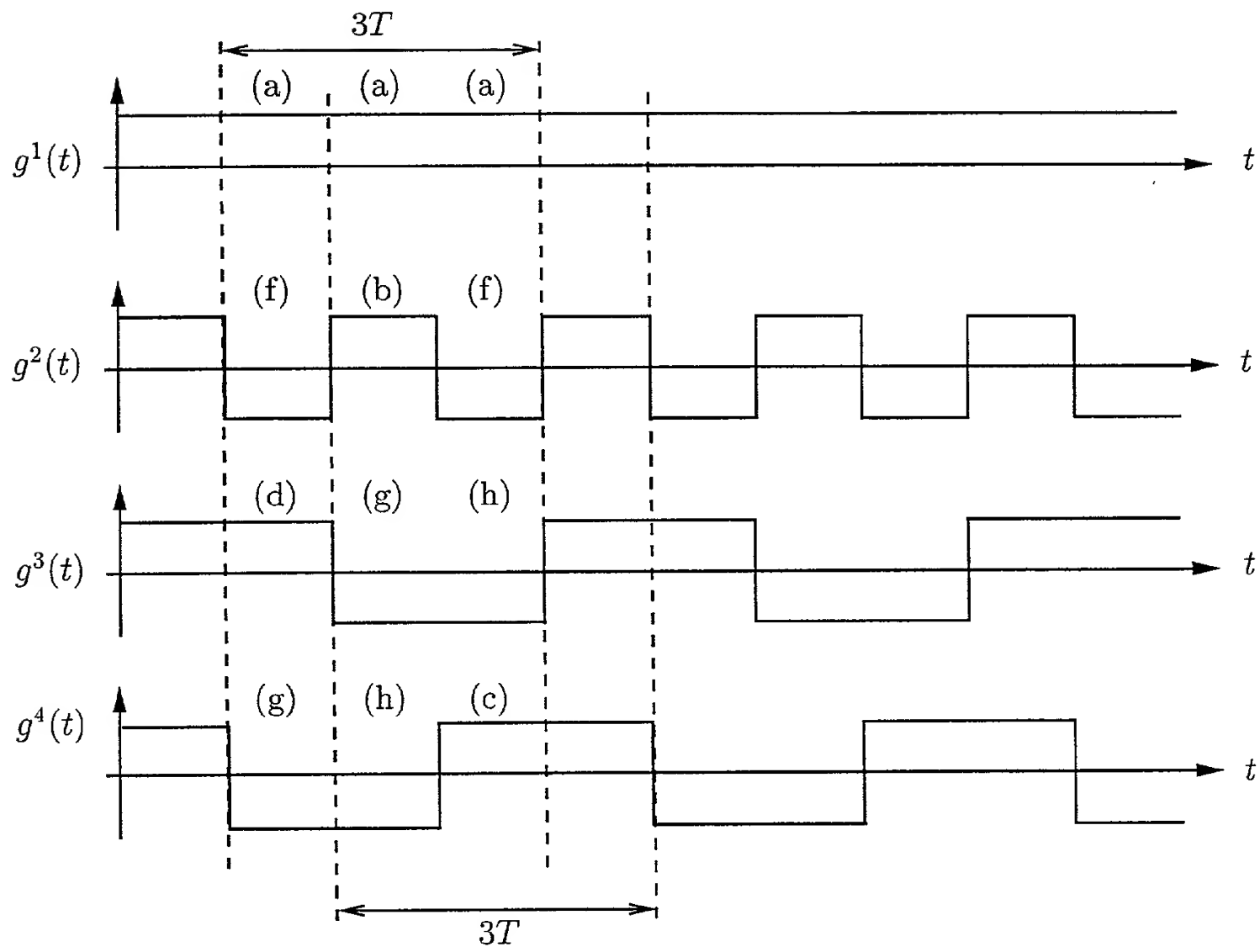
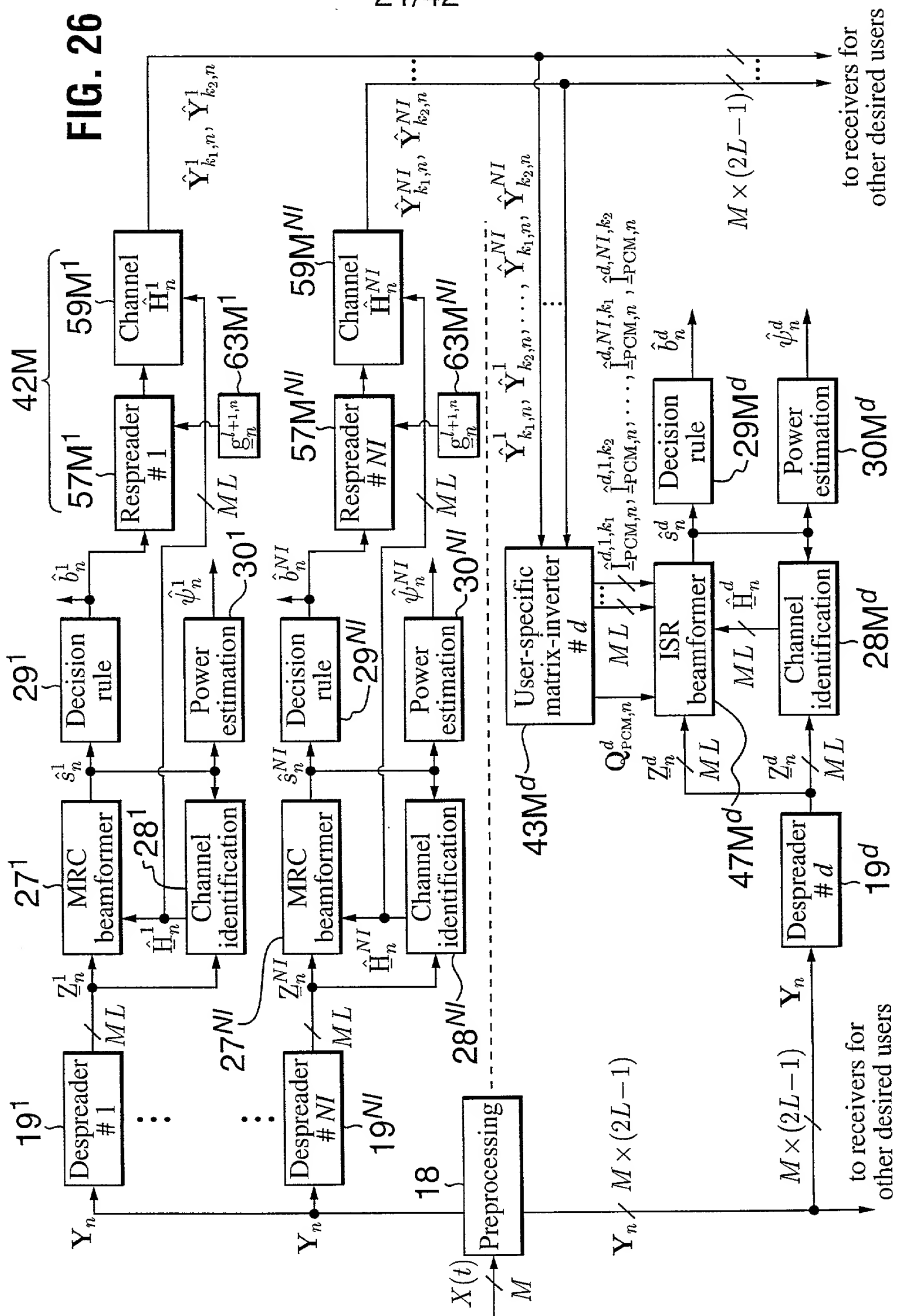
**FIG. 25**

FIG. 26



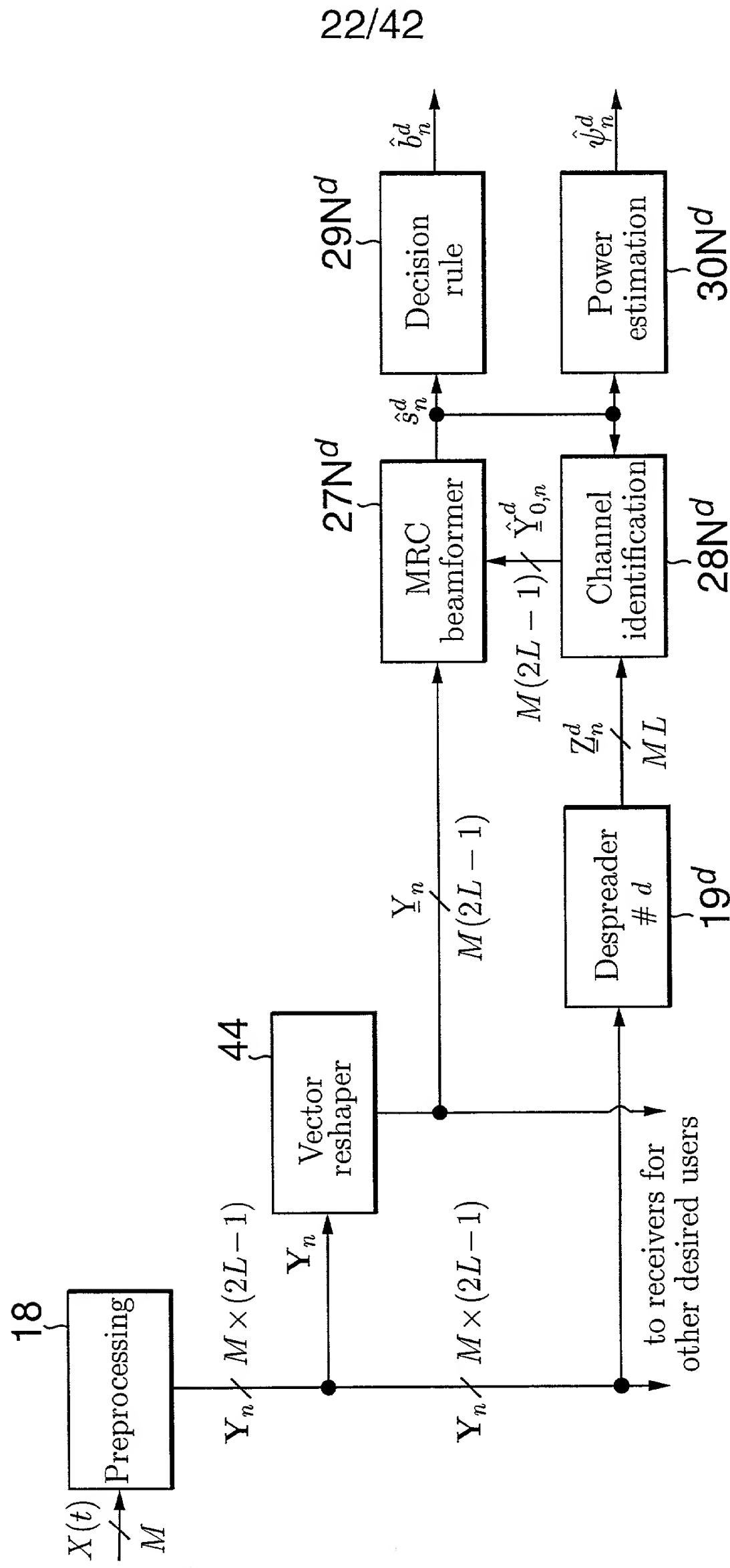


FIG. 27



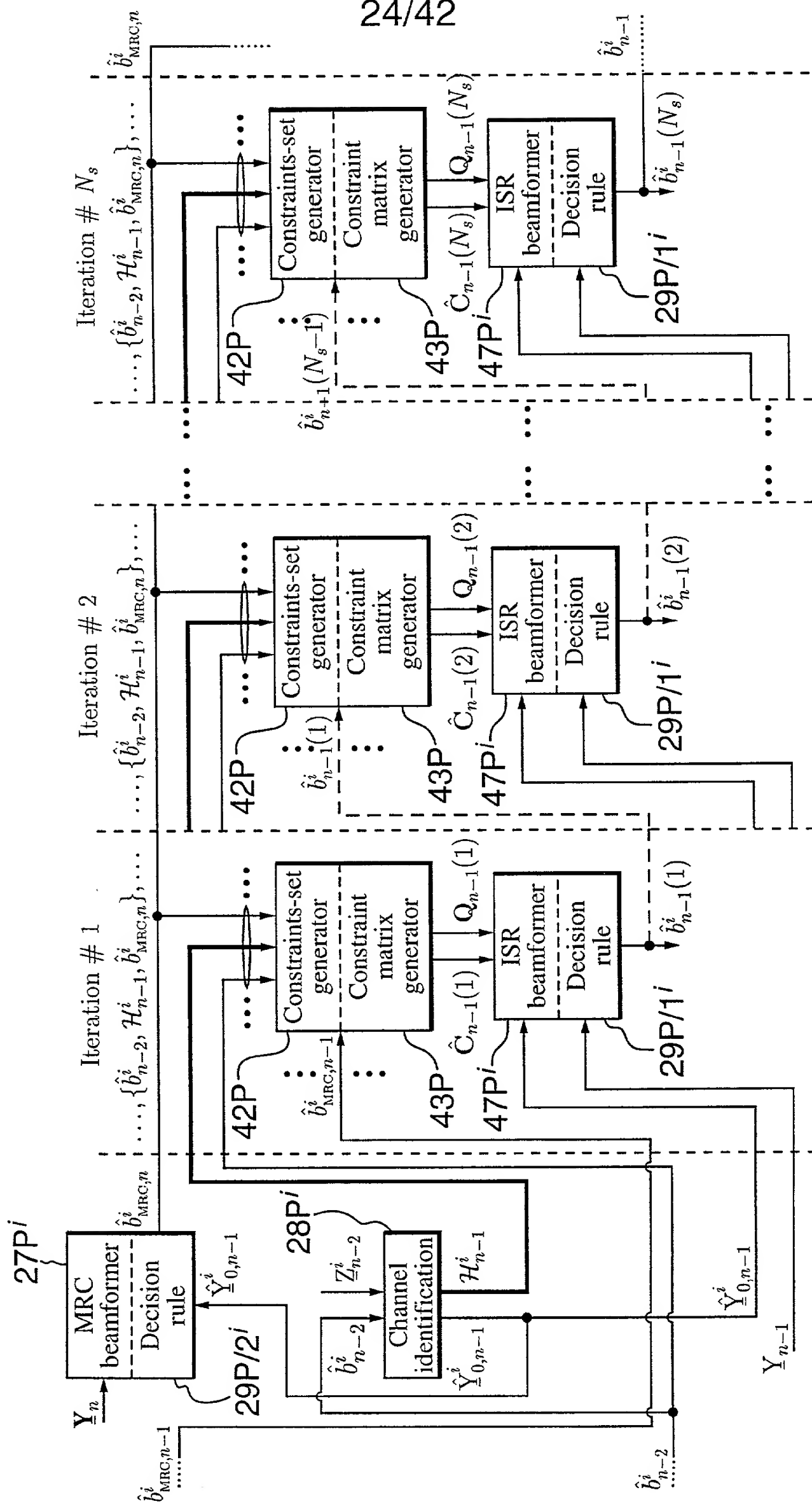
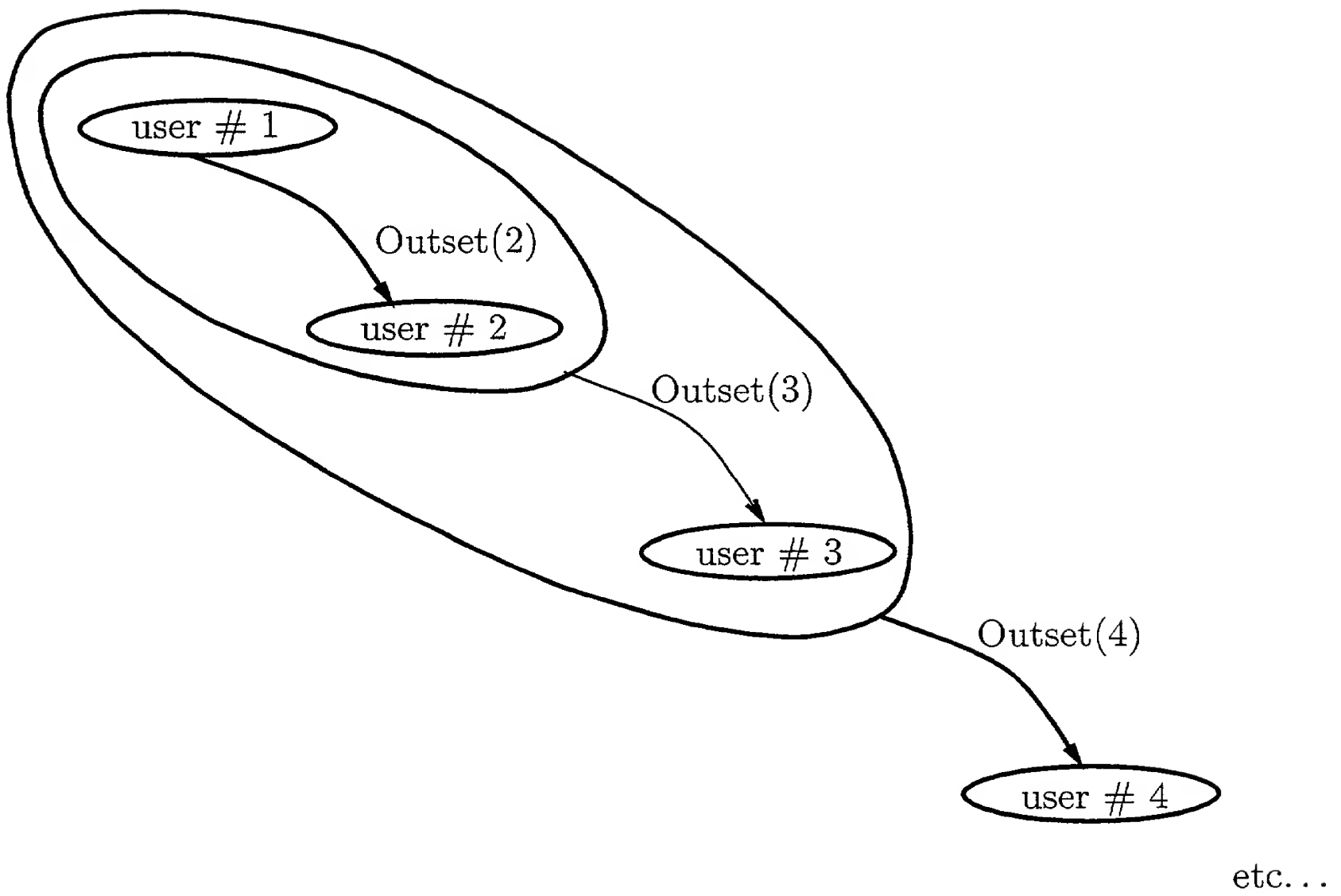


FIG. 29



**FIG. 30**

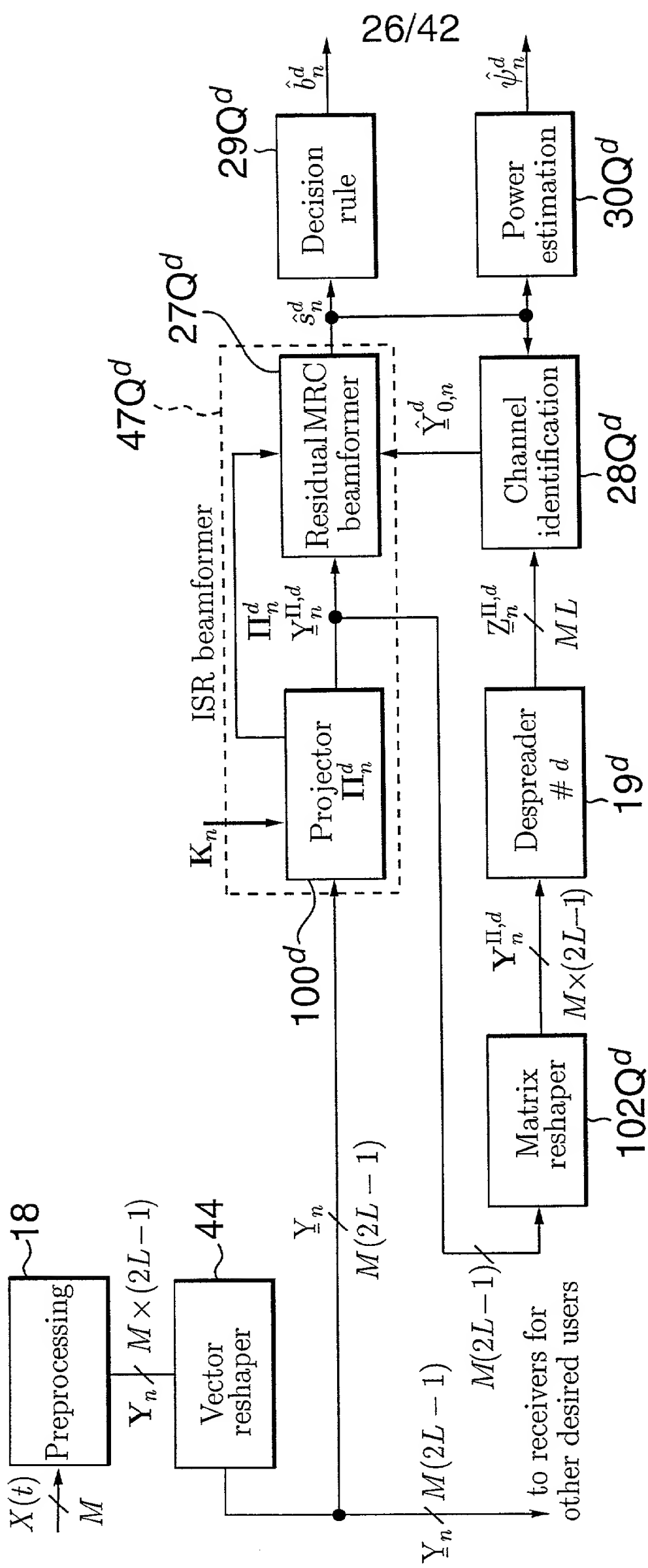


FIG. 31

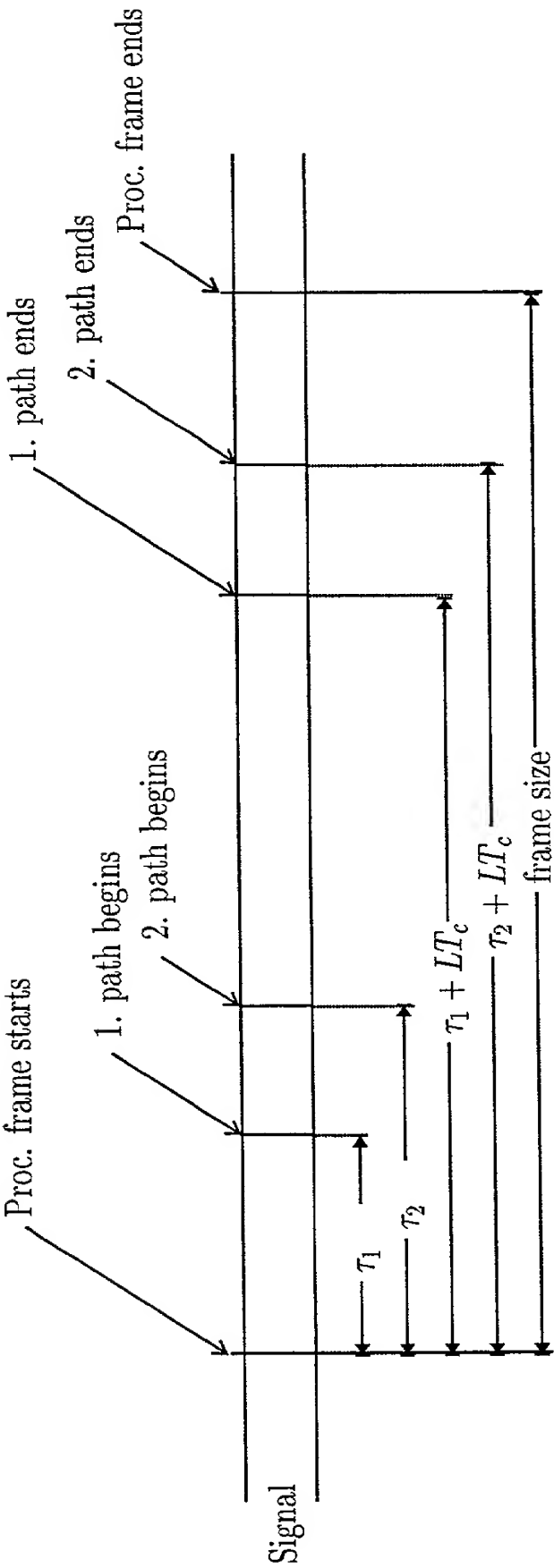


FIG. 32

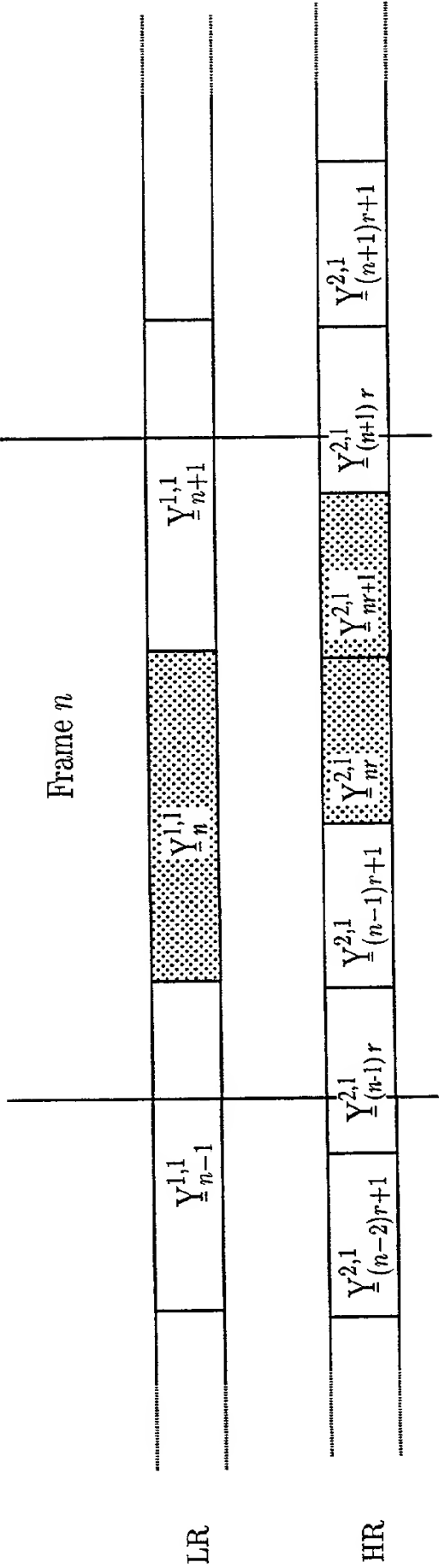


FIG. 33

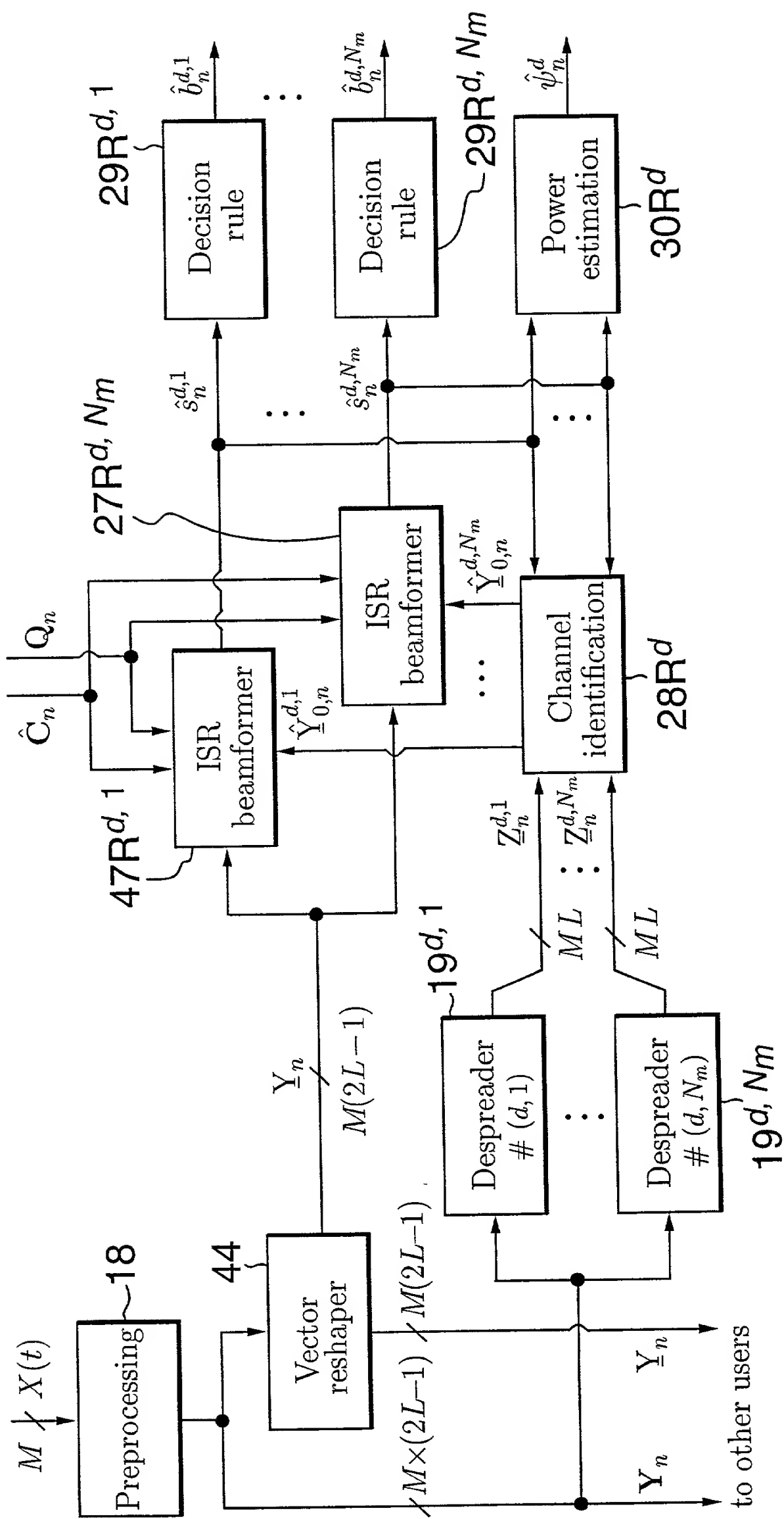


FIG. 34

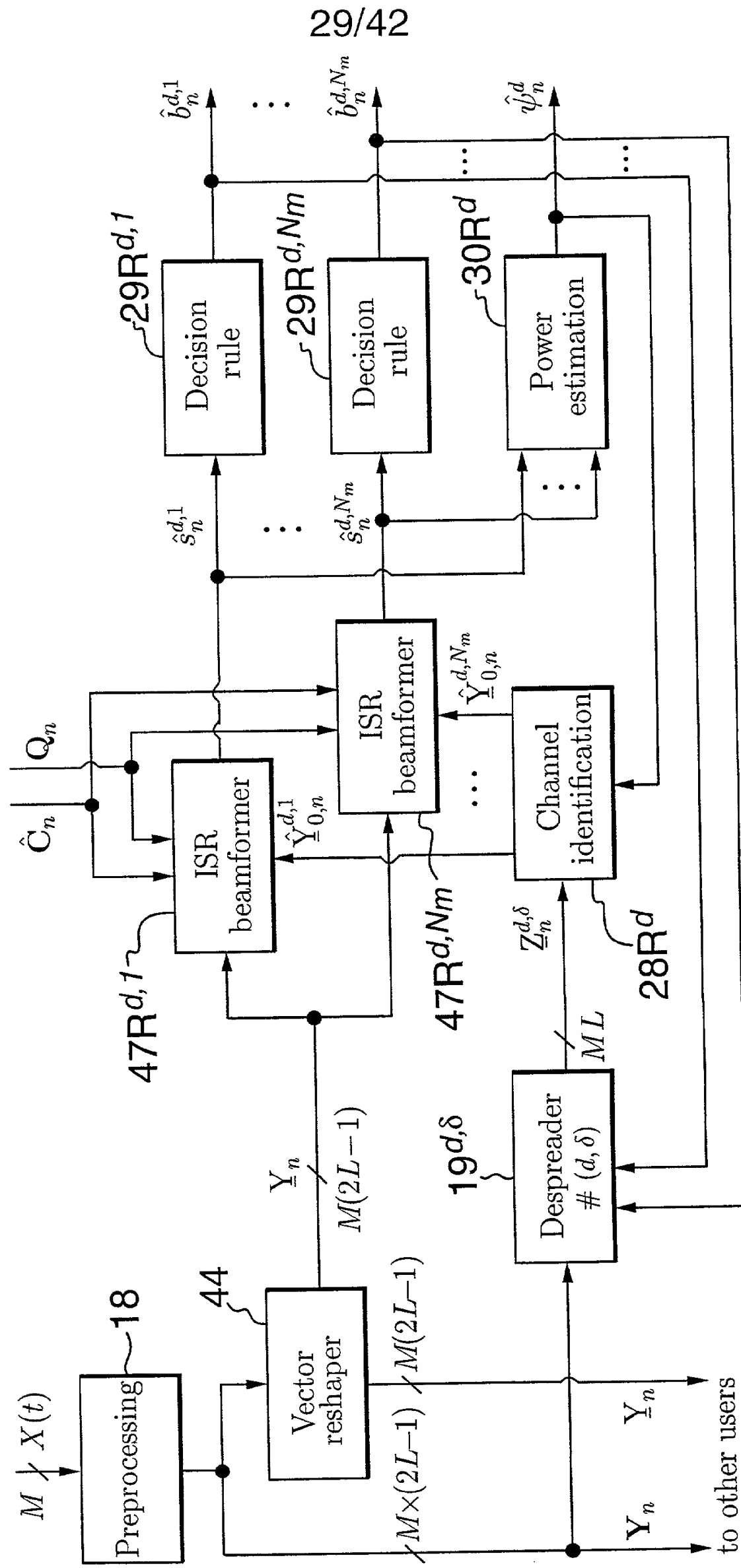


FIG. 35

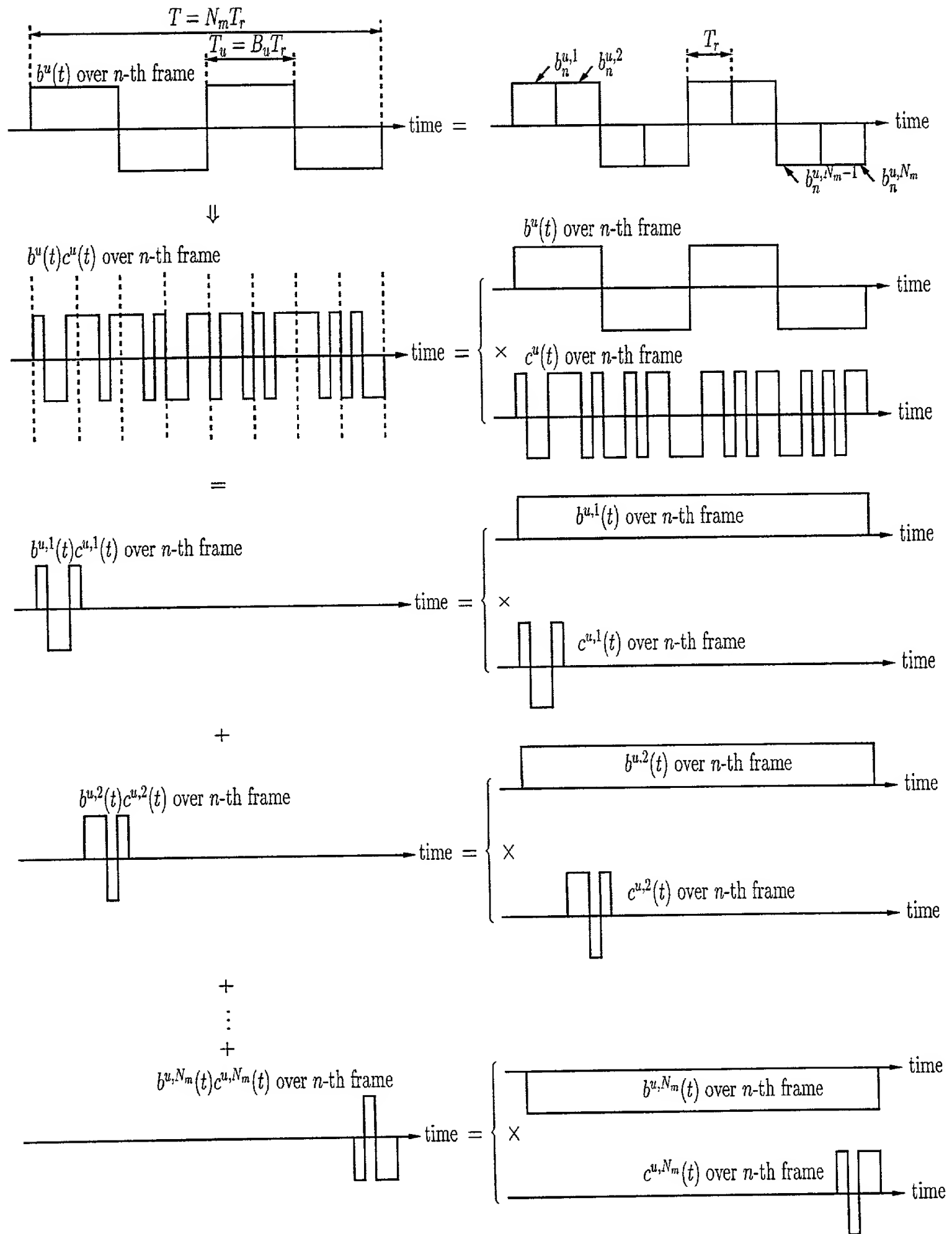


FIG. 36



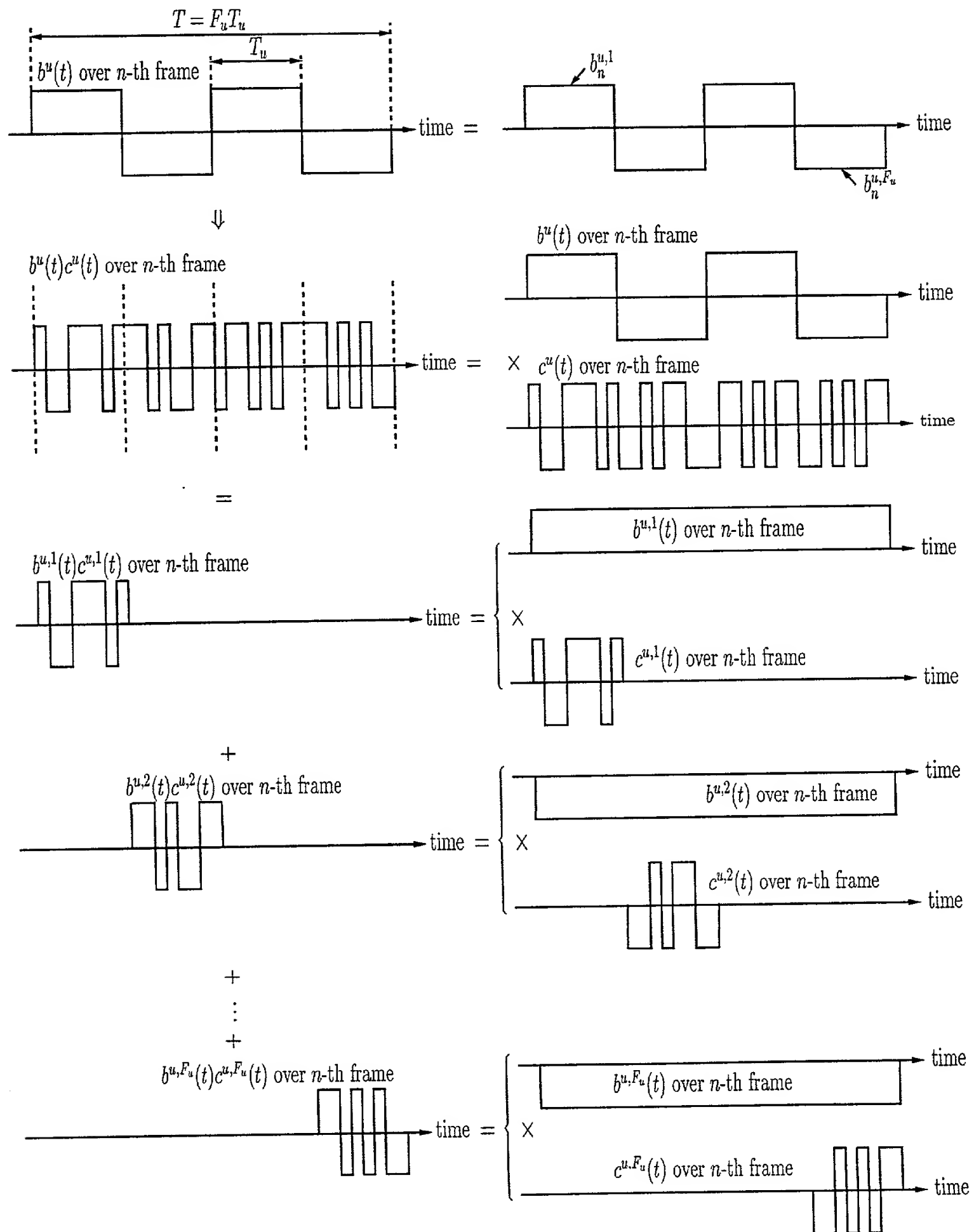


FIG. 38



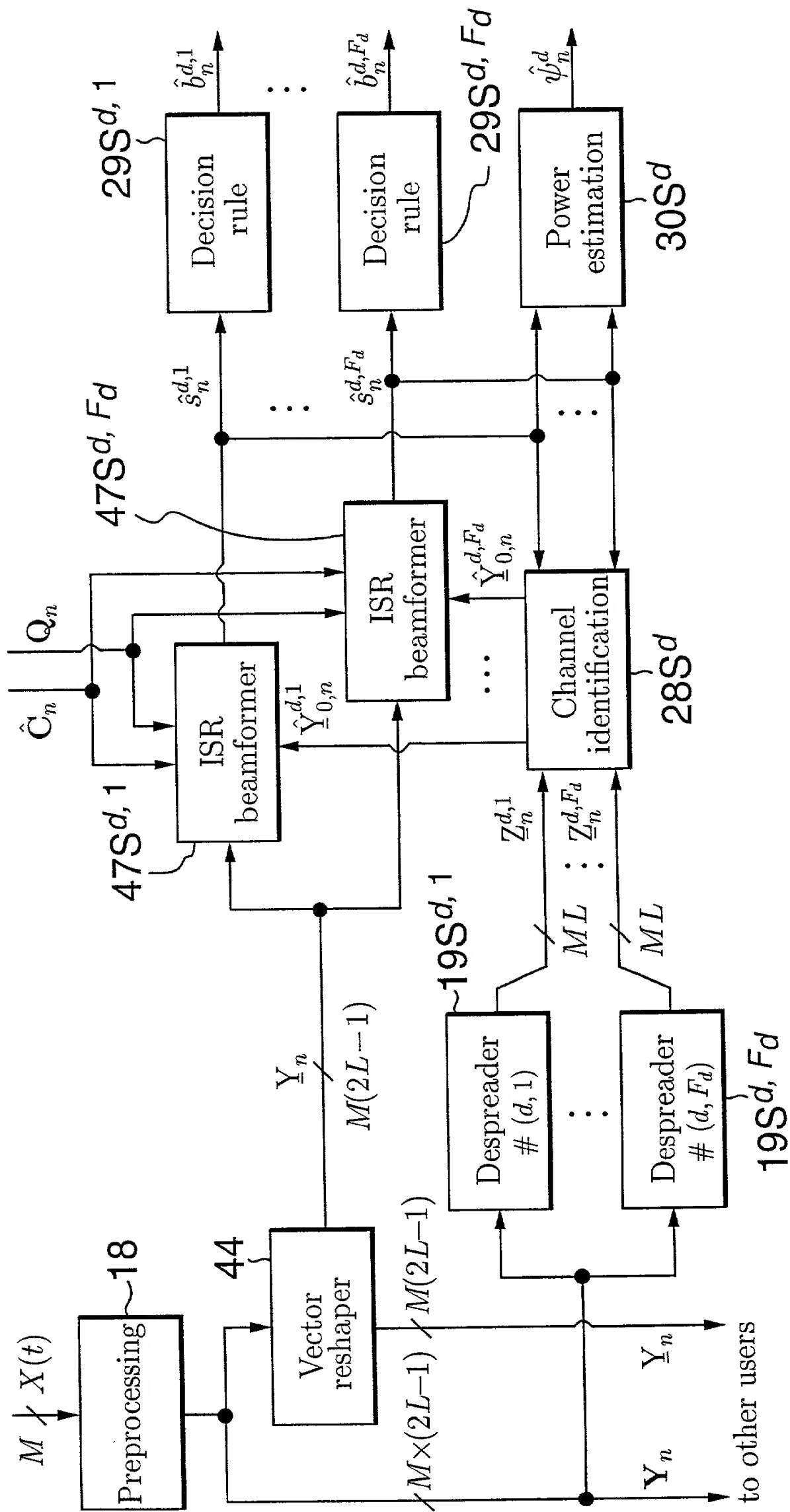
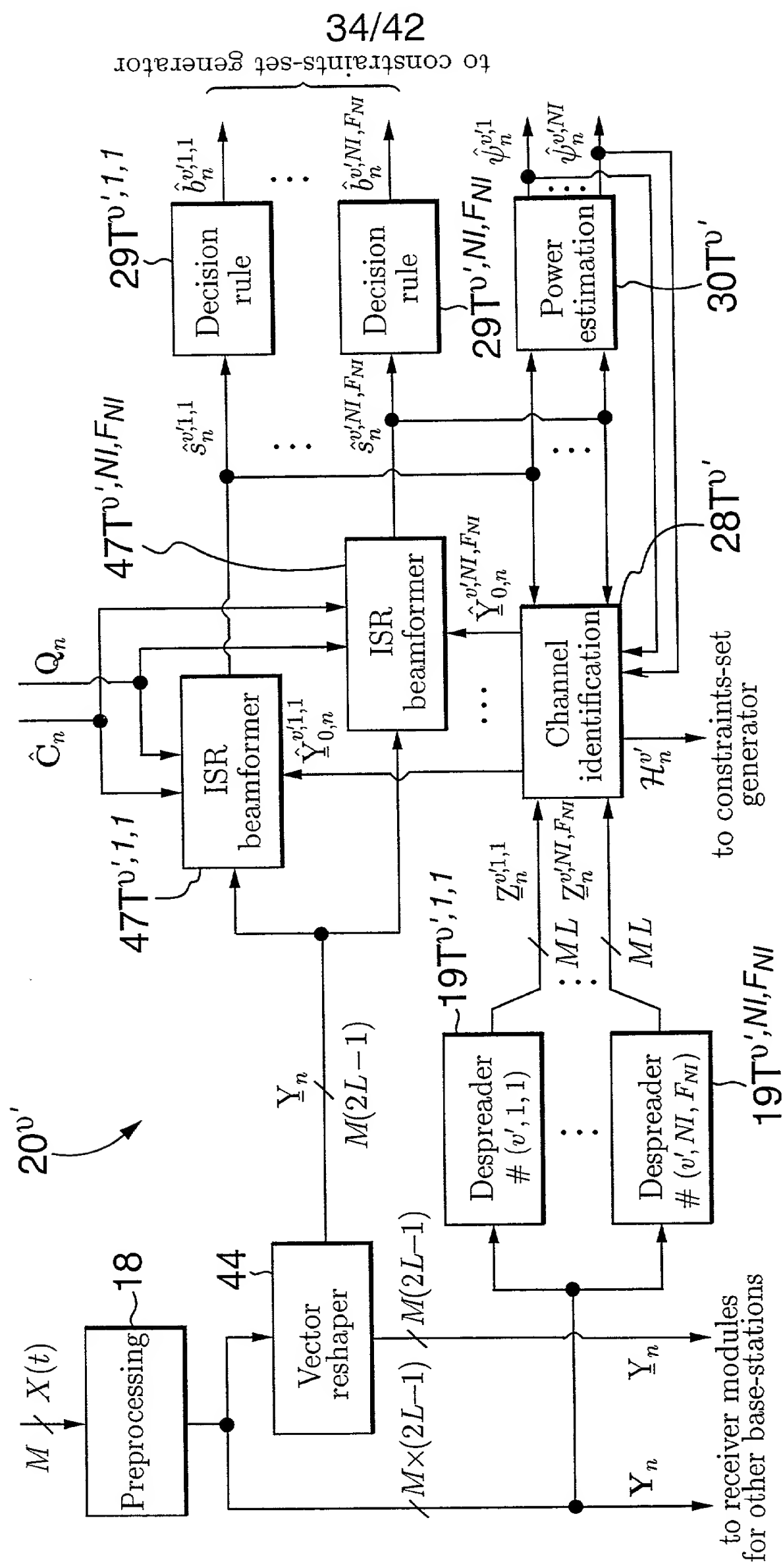


FIG. 39



**FIG. 40**

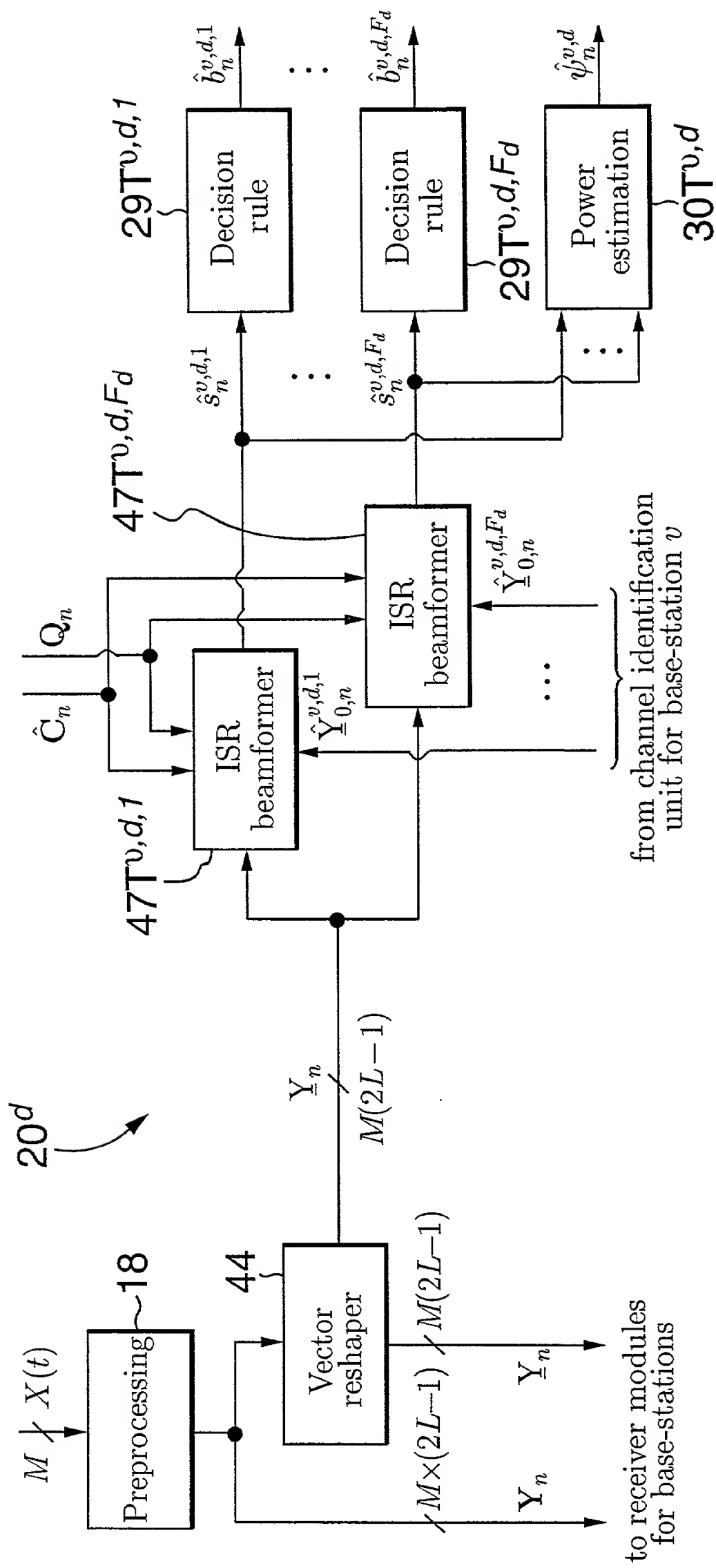
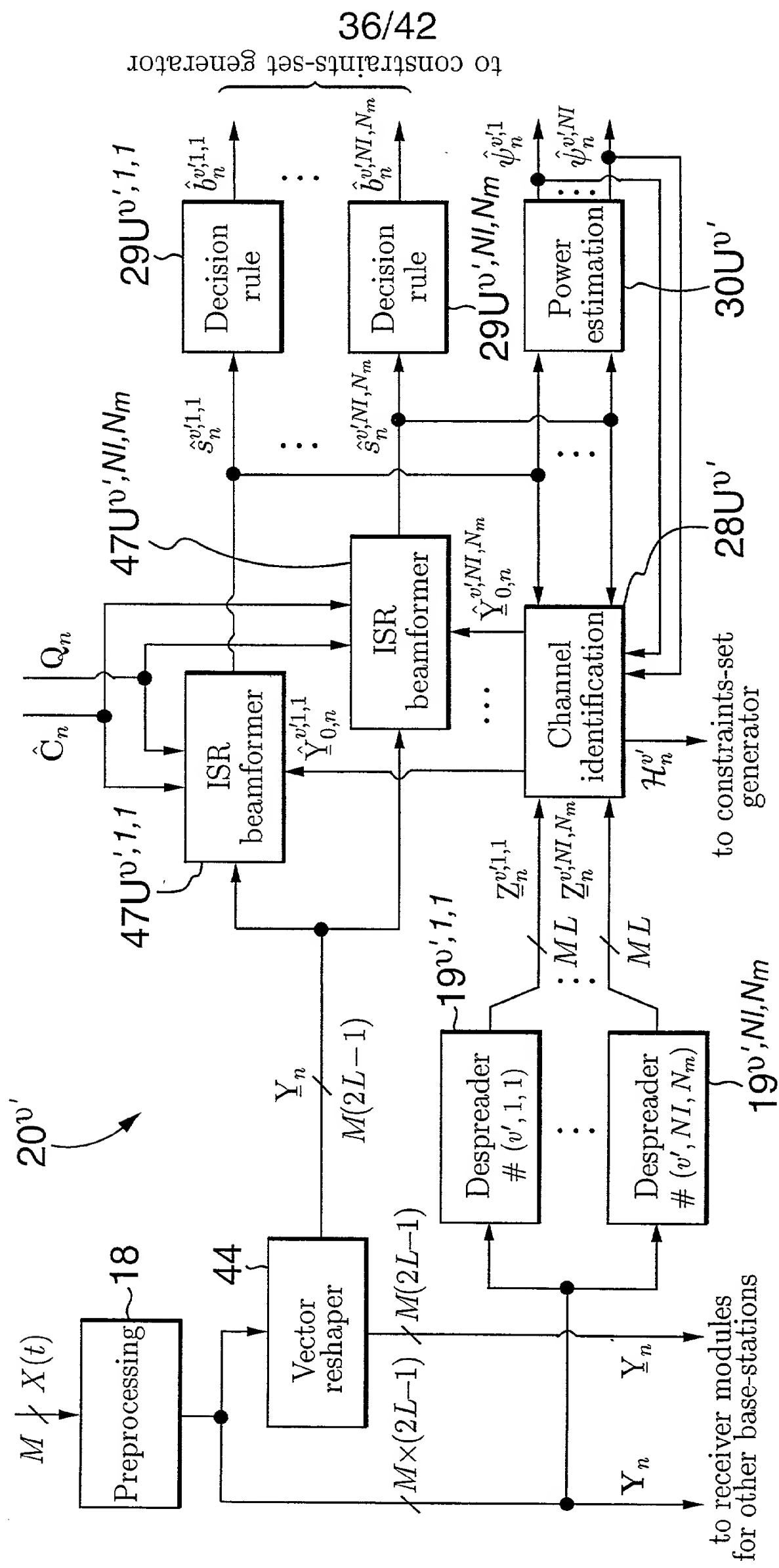


FIG. 41



**FIG. 42**

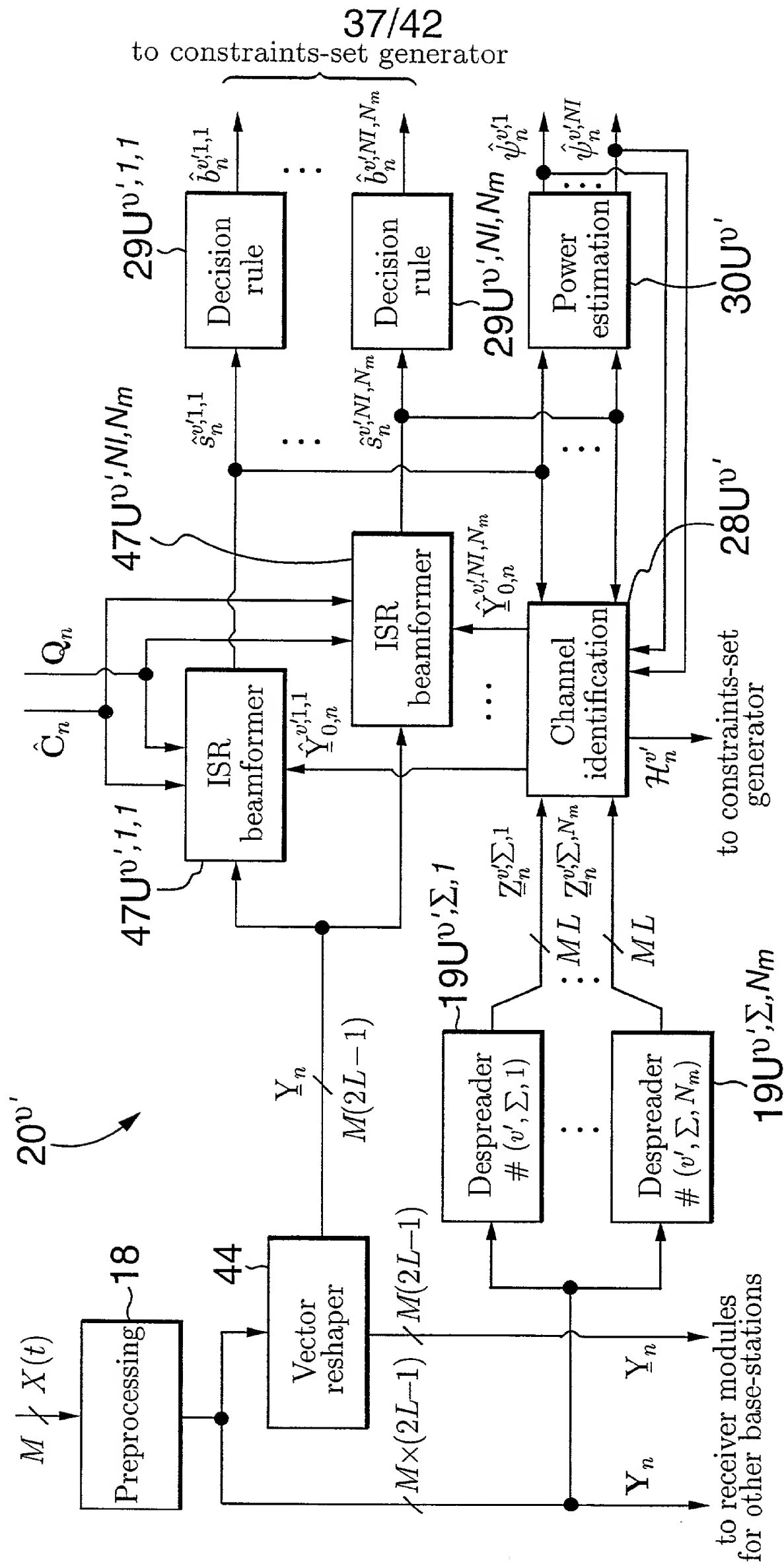
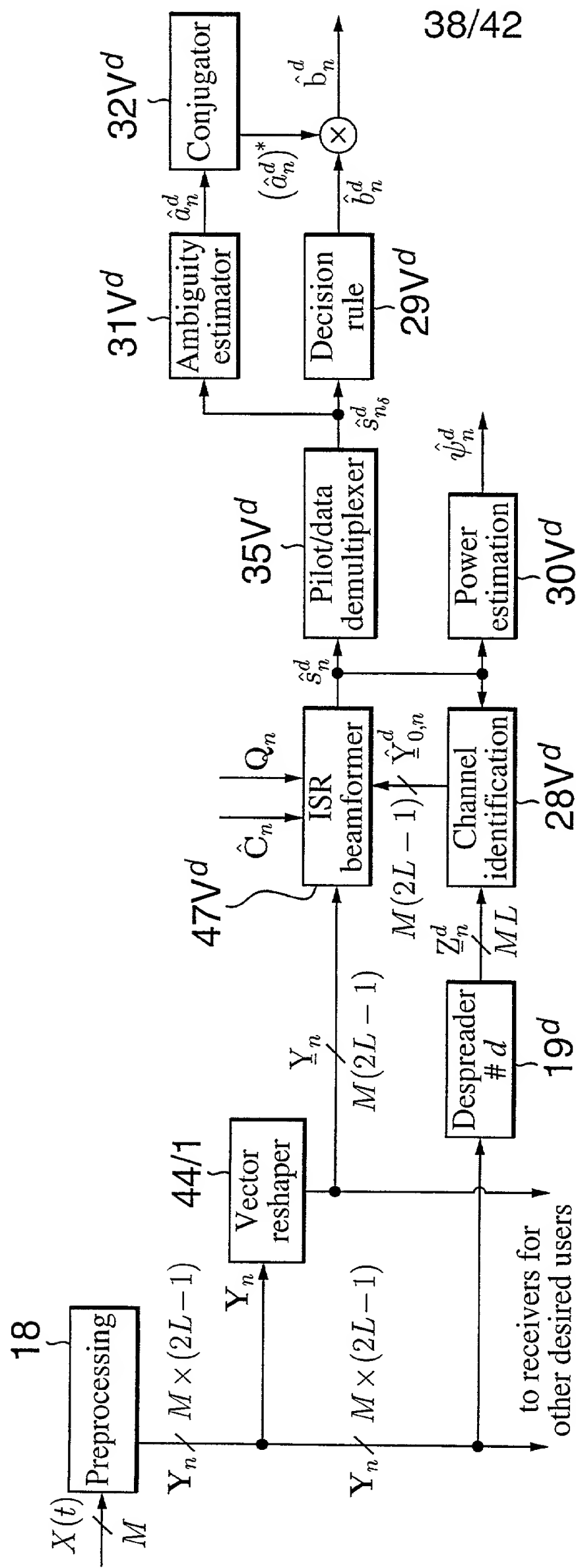
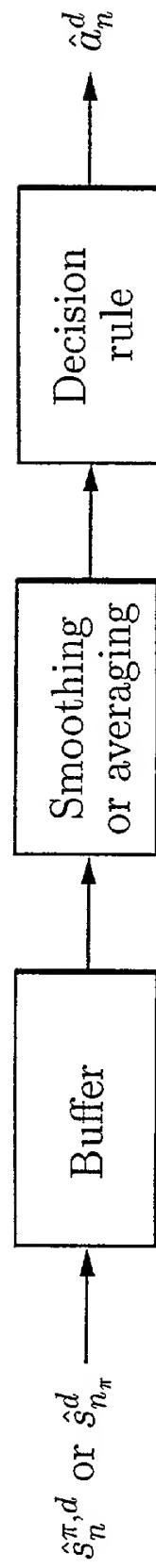


FIG. 43



**FIG. 44**



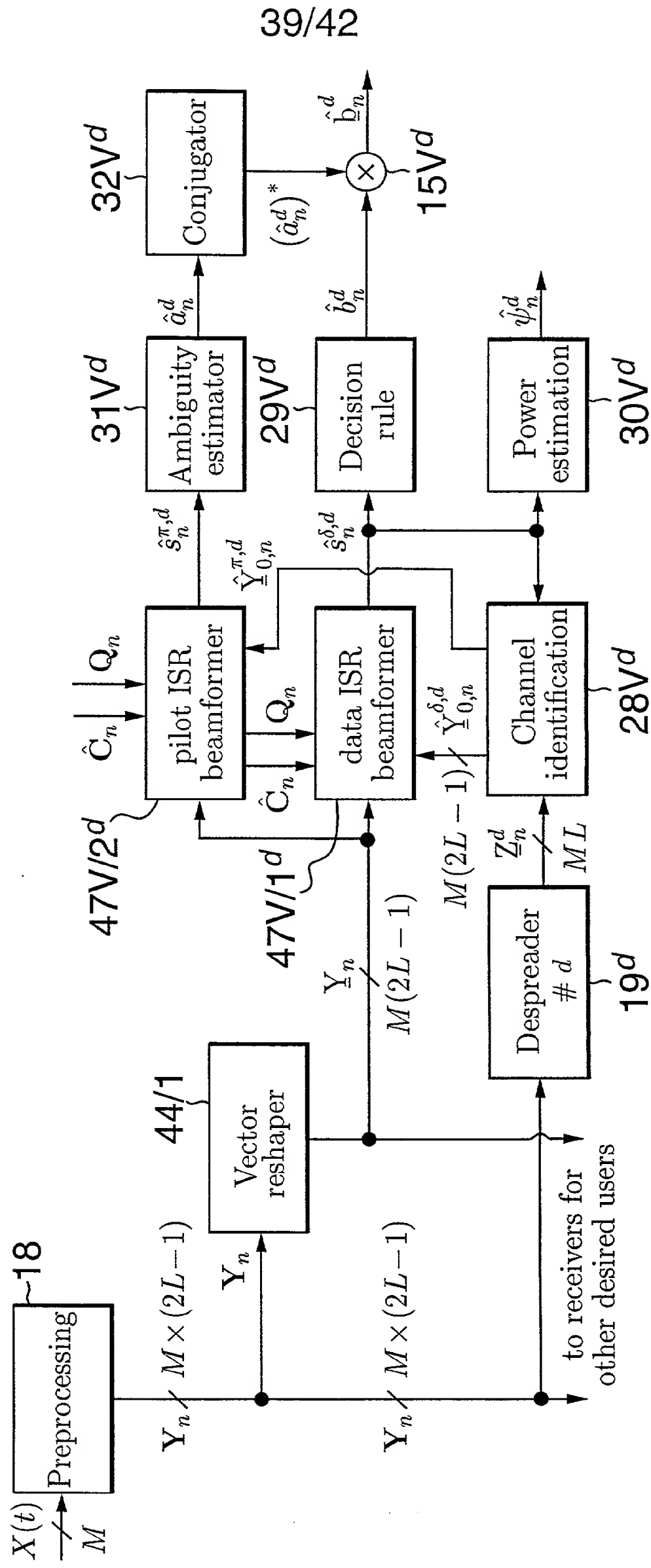


FIG. 46

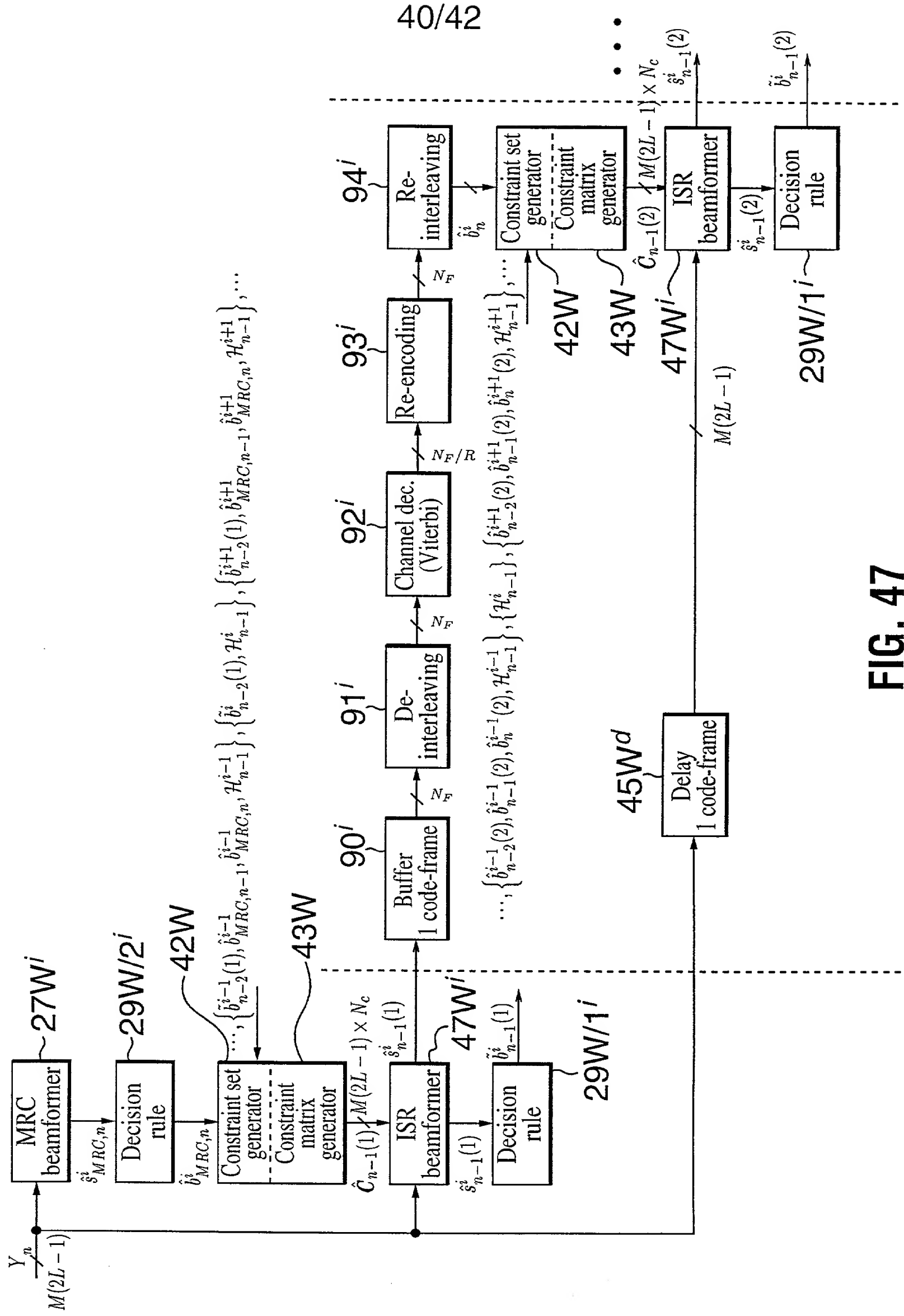


FIG. 47



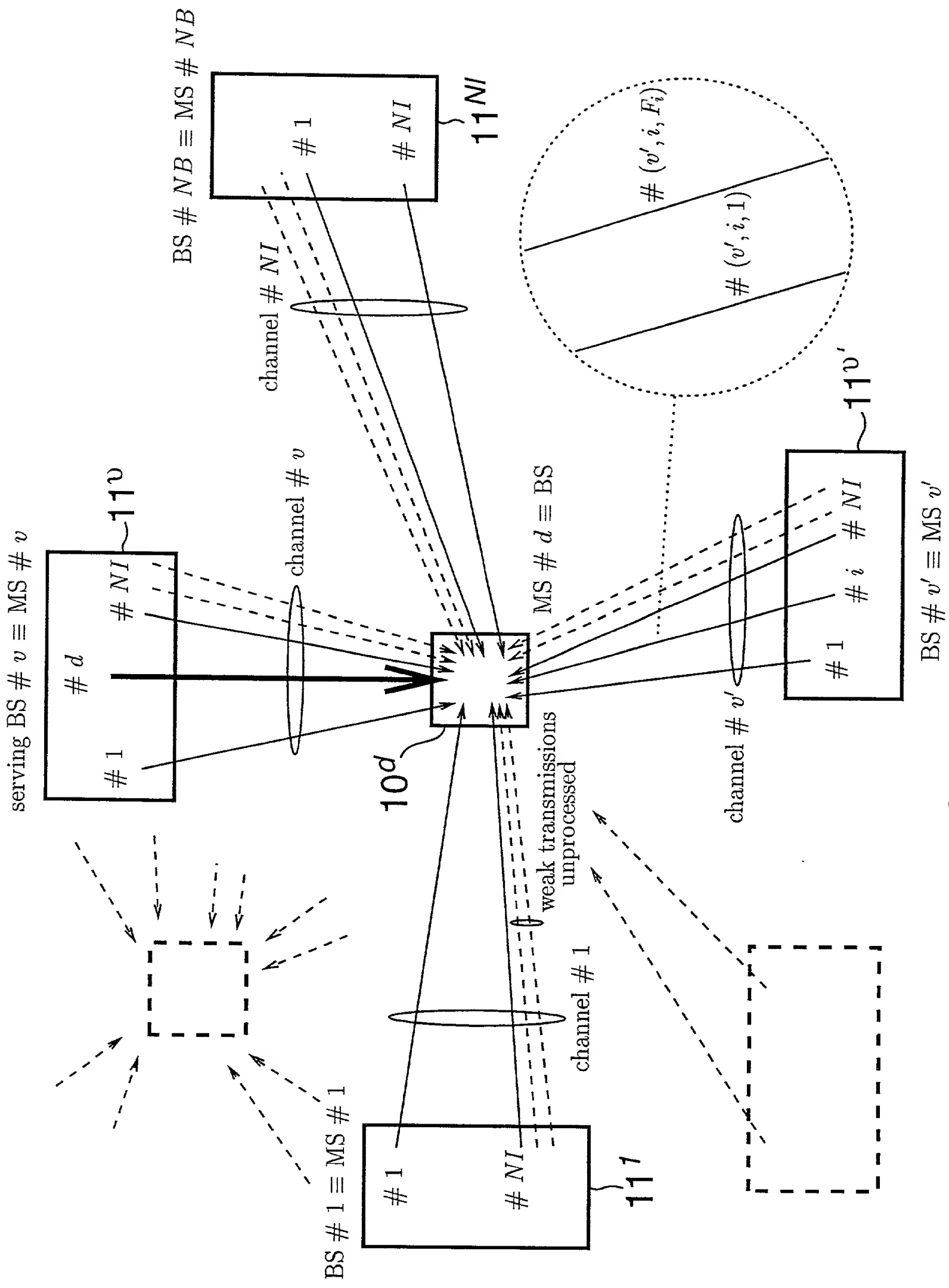


FIG. 48

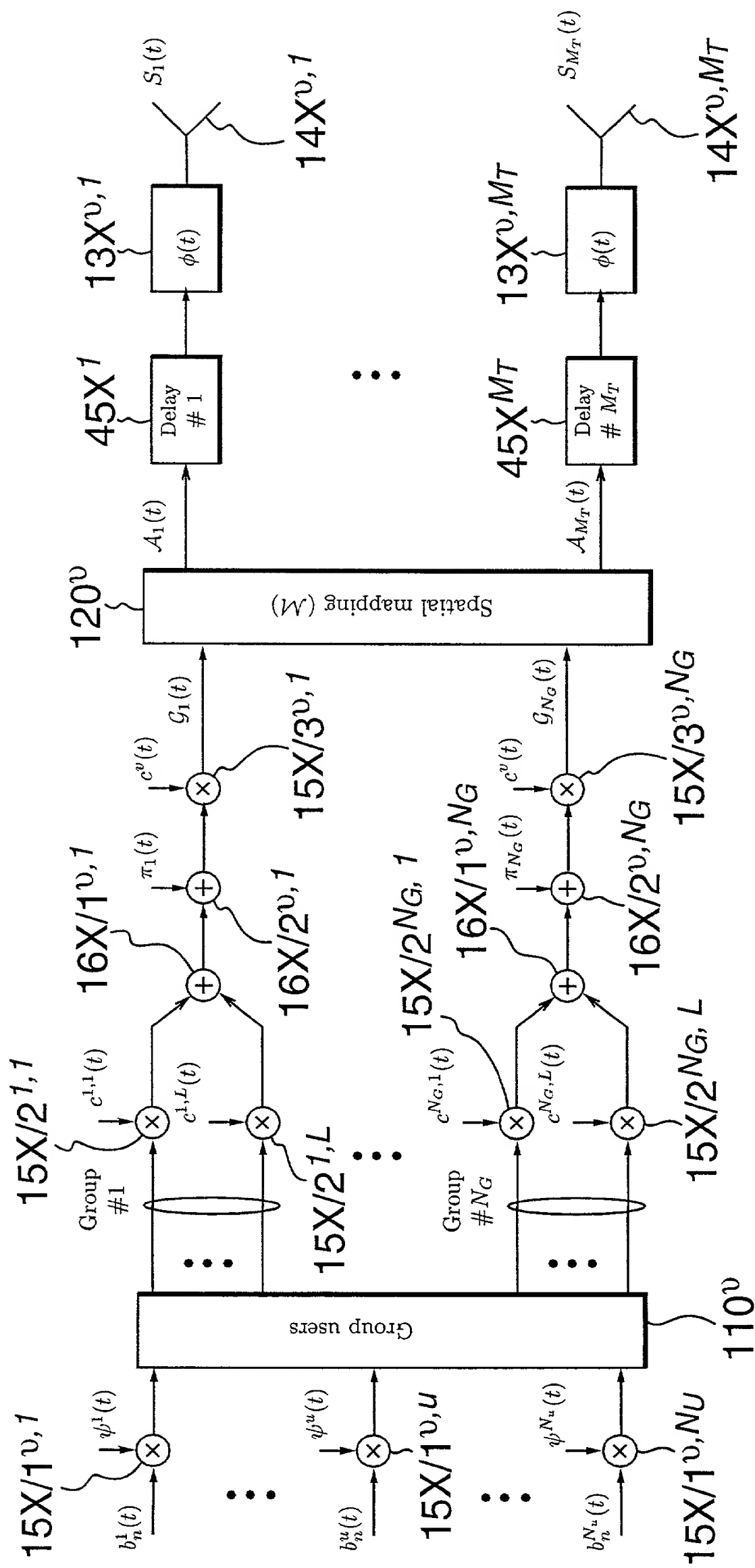


FIG. 49